## YASNAC J50 PC SYSTEM INSTRUCTIONS

CNC SYSTEM FOR TURNING APPLICATIONS / MACHINING CENTERS

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## CONTENTS

1. OUTLINE 1
2. BLOCK DIAGRAM
3. SPECIFICATIONS 2
3.1 FUNDAMENTAL SPECIFICATIONS ..... 2
3.2 PROGRAM FUNCTIONS ..... 2
3.3 MACRO INSTRUCTIONS 2
3.4 INPUT/OUTPUT SPECIFICATIONS ..... 2
4. PROCEDURES FOR SEQUENCE PROGRAM PREPARATION 4
5. ADDRESS NUMBER AND ADDRESS MAP ..... 5
5.1 ADDRESS NUMBER ..... 5
5.2 ADDRESS MAP AND DISPLAY SYMBOL ..... 5
5.3 I/O LIST AND SEQUENCE LADDER ..... 12
6. SEQUENCE CONTROL METHOD ..... 13
6.1 DIFFERENCES IN OPERATION ..... 13
6.2 SCANNING TIME (PROCESSING TIME) ..... 13
6.3 MEMORY CAPACITY OF SEQUENCE PROGRAM 14
7. PC INSTRUCTIONS ..... 15
7.1 PRELIMINARY KNOWLEDGE 15
7.2 TYPES OF INSTRUCTIONS AND LISTS ..... 15
7.3 INSTRUCTIONS FOR RELAYS ..... 19
7.4 INSTRUCTIONS FOR TIMERS ..... 22
7.5 INSTRUCTIONS FOR REGISTERS ..... 22
7.6 CONTROL INSTRUCTIONS ..... 30
7.7 MACRO INSTRUCTIONS ..... 31
8. SEQUENCE PROGRAM EXAMPLE ..... 43
8.1 SERIES CONNECTION ..... 43
8.2 PARALLEL CONNECTION ..... 43
8.3 SERIES AND PARALLEL CONNECTION ..... 43
8.4 MASTER CONTROL RELAY APPLICATIONS ..... 44
9. SEQUENCE PROGRAM ONLINE EDITING SYSTEM ..... 45
9.1 BLOCK DIAGRAM OF SEQUENCE PROGRAM EDIT SYSTEM ..... 45
9.2 SEQUENCE PROGRAM EDITOR (JDU01) ..... 46
9.3 CONNECTING SEQUENCE PROGRAM EDITOR 46
9.4 EDIT SYSTEM OPERATOR'S STATION ..... 47
9.5 FUNCTION MODE OF EDIT SYSTEM ..... 48
9.6 HOW TO ENTER EDITING SYSTEM MODE ..... 49
9.7 EDITING MODE (MODE 1) ..... 50
9.8 LIST TAPE INPUT/OUTPUT MODE (MODE 2 ) ..... 54
9.9 P-ROM WRITER MODE (MODE 3) ..... 57
9.10 PARAMETER MODE (MODE 4) ..... 59
9.11 PC DATA TABLE EDIT MODE (MODE 5) ..... 62
9.12 ADDRESS CHECK MODE (MODE 6) 6
9.13 RETURN TO NC SYSTEM MODE (MODE 4) ..... 64
9.14 OPERATING PROCEDURE ..... 65
10. SEQUENCE PROGRAM OFFLINE EDITING SYSTEM ..... 67
10.1 OUTLINE OF OFFLINE EDITING SYSTEM ..... 67
10.2 SOURCE FILE ..... 68
10.3 COMPILER 7010.4 LINKER 71
10.5 CHANGING INTO ROM ..... 72
10.6 JSD LADDER SOURCE CONVERTER ..... 73
10.7 LIST OF ERROR MESSAGES ANDWARNING MESSAGES 73
10.8 NOTES ..... 73
APPENDIX 1I/O LIST FOR YASNAC J50L(FOR LATHES) 74
APPENDIX 2
I/O LIST FOR YASNAC J50M
(FOR MACHINING CENTERS) ..... 85
APPENDIX 3LIST OF INTERNAL RELAYS, REGISTERSFOR YASNAC J50L/J50M 101
APPENDIX 4CONVERSION TABLE OF DECIMAL ANDHEXADECIMAL NOTATION 123

## INDEX

Subject Chapter Section Page
A ADDRESS CHECK MODE (MODE 6) ..... 9.... 9.12 ..... 62
ADDRESS MAP AND DISPLAY SYMBOL ..... 5.2 ..... 5
ADDRESS NUMBER ..... 5 ..... $5 \cdots 5.1$
ADDRESS NUMBER AND ADDRESS MAP ..... 5
APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) APPENDIX 1 ..... 74
APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) APPENDIX 2 ..... 85
APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M APPENDIX 3 ..... 101
APPENDIX 4 CONVERSION TABLE OF DECIMAL AND
HEXADECIMAL NOTATION APPENDIX 4 ..... 123
B BLOCK DIAGRAM ..... 2 ..... 1
BLOCK DIAGRAM OF SEQUENCE PROGRAM EDIT SYSTEM ..... 9 ..... 45
C CHANGING INTO ROM ..... 10
10.5 ..... 72
COMPIER ..... 70
0.3
Compiler Checking Items ..... 10 ..... 71
Compiler Operation ..... 70
CONNECTING SEQUENCE PROGRAM EDITOR ..... 46
CONTROL INSTRUCTIONS ..... 30
D DIFFERENCES IN OPERATION ..... 6 ..... 13
E EDIT SYSTEM OPERATOR'S STATION ..... 47
EDITING MODE (MODE 1) ..... 50
Editing of PC Data Tables ..... 62
Error List of Compile ..... 70 ..... 10
Execution Files 0 . . . . . 10.1.2 ..... 67
F Format of Source File ..... 10 ..... 68
FUNCTION MODE OF EDIT SYSTEM ..... 48
FUNDAMENTAL SPECIFICATIONS
H HOW TO ENTER EDITING SYSTEM MODE
| I/O LIST AND SEQUENCE LADDER ..... 12
INPUT/OUTPUT SPECIFICATIONS ..... 2
INSTRUCTIONS FOR REGISTERS ..... 22
INSTRUCTIONS FOR RELAYS ..... 19
INSTRUCTIONS FOR TIMERS ..... 22
J JSD LADDER SOURCE CONVERTER ..... 10
10.6 ..... 73
L Line Connection 10.5.2 ..... 72
LINKER 10.4 ..... 71
Linker Operation 10.4 .2 ..... 71
Linker Output File 10.4.3 ..... 72
LIST OF ERROR MESSAGES AND WARNING MESSAGES 10.7 ..... 73
LIST TAPE INPUT/OUTPUT MODE (MODE 2) ..... 54
M MACRO INSTRUCTIONS ..... 2
MACRO INSTRUCTIONS ..... 31
MASTER CONTROL RELAY APPLICATIONS ..... 44
MDI Write Operation on Sequence Program ..... 52
MEMORY CAPACITY OF SEQUENCE PROGRAM ..... 14
N NOTES ..... 10 ..... 73
O Object Data and Linker Processing ..... 10
Operation Environment
10
OPERATING PROCEDURE ..... 6571OUTLINE
Outline of Execution File Processing ..... 0 ..... 67
OUTLINE OF OFFLINE EDITING SYSTEM ..... 10
10.1 ..... 67
Outline of Operation ..... 10
10.1.4 ..... 67

## INDEX (Cont'd)

Subject Chapter Section Page
P P-ROM Format Tape Input/Output Function (IN , OLT) ..... 9 ..... 53
P-ROM WRITER MODE (MODE 3) ..... 9.9 ..... 57
8.2
PARALLEL CONNECTION ..... 43
9.10
PARAMETER MODE (MODE 4) ..... 59
9 .... 9.11 pC DATA TABLE EDIT MODE ..... 62
PC INSTRUCTIONS ..... 15
PRELIMINARY KNOWLEDGE ..... 7.1 ..... 15
PROCEDURES FOR SEQUENCE PROGRAM PREPARATION ..... 4
PROGRAM FUNCTIONS 3.2 ..... 2
R Reading-in, Punch-out, and Verify a P-ROM Format Tape (IN, OUT and VER operations) 9.11 .2 ..... 62
RETURN TO NC SYSTEM MODE (MODE 4) 9.13 ..... 64
$S$ SCANNING TIME (PROCESSING TIME) ..... 6.2 ..... 13
Selection of PROM Writer 10.5.1 ..... 72
SEGUENCE CONTROL METHOD ..... 13
Sequence Program Editing ..... 50
SEQUENCE PROGRAM OFFLINE EDITING SYSTEM ..... 67
SEGUENCE PROGRAM ONLINE EDITING SYSTEM ..... 45
SEQUENCE PROGRAM EDITOR (JDU01) ..... 46
SEQUENCE PROGRAM EXAMPLE ..... 43
SERIES AND PARALLEL CONNECTION ..... 43
SERIES CONNECTION 8.1 ..... 43
SOURCE FILE ..... 68
SOURCE FILES ..... 68
SPECIFICATIONS ..... 2
T TYPES OF INSTRUCTIONS AND LISTS 7.2 ..... 15
W When NC Unit Entered SD Mode from Offline State 9.13 .1 ..... 64
When NC Unit Entered SD Mode from Online State ..... 9.13 .2 ..... 64
When NC Unit is in Offline State (System No. $6 \rightarrow$ SD MODE) 9.6.1 ..... 49
When NC Unit is in Online State (System No. $4 \rightarrow$ SD MODE) 9.6 .2 ..... 49

## 1. OUTLINE

1. The programmable controller (called PC hereafter) for YASNAC J50L/J50M stands between the standard YASNAC NC unit and the machine tool. It facilitates the compact and efficient utilization of the sequence control required by the machine tool through the software.
2. Sequence program editing of PC can be performed efficiently with CRT; NC and JSD modes are easily changed and selected.
3. The PC is optional and it is installed in the NC unit, if selected.
4. In this manual, "PC programming method" (Selections 1 to 8 ) and "Sequence program editing unit and the operating method" (Sections 9 and 10) have been explained so that the users to facilitate the use of the above described PC.

## 2. BLOCK DIAGRAM

The block diagram of the PC system for YASNAC
$\mathrm{J} 50 \mathrm{~L} / \mathrm{J} 50 \mathrm{M}$ is shown in Fig. 2.1.


Fig. 2.1 Block Diagram of PC System For YASNAC J50L/J50M

- Solid line shows the YASNAC CNC unit provided with P.C.
- Broken line shows the sequence program edit system temporarily used by incorporating the sequence program edit system (JDUO1) in YASNAC.


## Note:

1. When the control is used as sequence program edit system, the operator's panel with CRT display changes to the sequence program edit panel.
2. Sequence program edit system (JDUO1) can be mounted on the CPU rack.
3. P-ROM writer which is commercially available may be used. It is used to write the completed sequence edited and checked into P-ROM.
4. Tape reader is used to load List Tape in which sequence ladder is coded or P-ROM Format Tape consisting of machine language into sequence edit system.
5. Tape puncher punches out the completed sequence edited and checked in the form of List Tape or P-ROM Format Tape.

## 3. SPECIFICATIONS

### 3.1 FUNDAMENTAL SPECIFICATIONS

(1) Control method: Scanning method
(2) Processing time:

Approx. $2.7 \mu \mathrm{sec} / \mathrm{step}$
High speed scanning time - 8 msec
Low speed scanning time $-8 \mathrm{msec} \times \mathrm{n}$ ( n is letermined by the capacity of the total program.)
(3) Program memory capacity :

Memory element : EPROM ( 1024 k bits/one)
Basic -64 k bytes ( 1 EPROM)
( 64 k bytes corresponds to approximately 16000 steps in basic instruction.)
(4) Types of instruction language :

Basic instruction - 59 types
Macro instruction-11 types

### 3.2 PROGRAM FUNCTIONS

```
(1) Internal relay: 4000 points
(2) Register: \(\quad 500\) ( 8 bits/one)
(3) Timer: \(\quad 94\) (5 types)
. \(8 \mathrm{msec}-2.4 \mathrm{sec}, 20 \mathrm{ea}\).
- \(50 \mathrm{msec}-12.75 \mathrm{sec}, 30 \mathrm{ea}\).
- \(100 \mathrm{msec}-25.5 \mathrm{sec}, 30 \mathrm{ea}\).
- \(1 \mathrm{sec}-255 \mathrm{sec}\), 10 ea.
- \(1 \mathrm{~min}-255 \mathrm{~min}, 4\) ea.
(4) Sequencer parameter: 100
(5) Keep relay: 7200
(6) Keep memory: 900 ( 8 bits/memory)
```


### 3.3 MACRO INSTRUCTIONS

Following 11 types of macro instructions can be used.

Instruction word
(1) Rise signal detection:
(2) Fall signal detection:
(3) Counter:

SUBP 005
Functions--Ring counter or preset counter or ip-down counter.
Sounting range--0 - 9999
(4) Rotation
SUBP 006
(5) Code conversion
SUBP 007
(6) Pattern clear

SUBP 009
(7) Parity check

SUBP 011
(8) Data conversion:

SUBP 014
(9) Data search

SUBP 017
(10) Index data transfer

SUBP 018
(11) Message display

SUBP 023 --
Optional
function

### 3.4 INPUT/OUTPUT SPECIFICATIONS

(1) Standard I/O boards < FC810 (FC860) >
(i) DC input:
112 points
(ii) Non contact output : 96 points
< FC861>
(i) DC input: 64 points
(ii) Non contact output: 56 points
(2) CRT panel built-in I/O boards <SP50-1>
(i) DC input: 64 points
(ii) Non contact output : 32 points
<SP50-2>
(1) DC input: 64 points
(ii) Non contact output: 56 points

Note:

1. The detail of basic instructions are given in the following table.

| Type of Instruction | No. of <br> Instructions |
| :--- | :---: |
| 1. Relay instruction | 13 |
| 2. Register instruction | 37 |
| 3. Timer instruction | 2 |
| 4. Control instruction | 7 |
| $\quad$ Total | 59 |

2. Internal relays and registers are the same. Addresses used as internal relays cannot be used as internal relays.
3. Keep relays and keep memories are the same. Addresses used as keep relays cannot be used as keep memories. Addresses used as keep memories cannot be used as keep relays.
(3) I/O board location

5 I/O boards are shown below.

(4) Maximum number of each I/O board

## Maximum

| - Standard I/O board (FC810, FC860) | 3 | Input: $\quad 336$ points Output : 288 points |
| :---: | :---: | :---: |
| Standard I/O board (FC861) | 7 | Input : 448 points <br> Output : 392 points |
| CRT panel built-in 1/O board (SP50-1) | 1 | Input: 64 points Output : 32 points (56 points for SP-2) |

## Notes :

- YASNAC J50 needs at least 1 of CRT panel built-in I/O board (SP50-1 or SP50-2). Therefore, a max. of 3 (max. input : 400 points, max. output : 344 points) for adding only FC810/FC860 and a max. of 7 (max. input : 512 points, max. output : 448 points) for adding only FC861 can be connected.
- Several I/O boards can exist at the same time, without exceeding the allowance I/O area No.
The number of I/O boards can be expanded. Therefore, the last board needs to be terminal scanned.


## 4. PROCEDURES FOR SEQUENCE PROGRAM PREPARATION




## 5. ADDRESS NUMBER AND ADDRESS MAP

### 5.1 ADDRESS NUMBER

In the preparation of the sequence program, the I/O signals of PC, internal relay, timer, battery backed-up memory, etc. of PC are all designated by address No. (4-digit number following mark \#) and bit number ( $0-7$ bit).

$\left[\begin{array}{l}(A) \text { Name of } 8 \text { points of signal or } \\ (B) \text { Name of } 1 \text { byte }(=8 \text { bits }) \text { of data }\end{array}\right.$
(1) Designation of I/O Signals, Internal Relays, etc. (1 Bit Element)

As shown below, the elements which can be indicated by $l$ bit information are designated by 5 digits (address no. and bit no.) preceded by the mark \#.

| Element | Name |
| :--- | :---: |
| 1. I/O signal | \# |
| 2. Internal relay |  |
| 3. Keep relay | Address No. |

In the case, the address No. takes the meaning of above (A) and it can be taken as the name given with respect to the 8 points of the signal.
(2) Designation of Register, Timer, etc.
(1 Byte Element)
The elements having 1 byte ( $=8$ bits) information, as shown below, are designated only by address number. In this case, the address number takes the meaning of above ( $B$ ) and it can be taken as the name given with respect to 1 byte data.

| Element | Name |
| :--- | :--- |
| 4. Register | \# |
| 5. Timer |  |
| 6. Sequencer parameter |  |
| 7. Keep memory |  |

Note: Depending on the instruction, naming of
2 bytes \#1500 and \#1501 can be carried out through the address name \#1500. Example: PUSH \#1500

### 5.2 ADDRESS MAP AND DISPLAY SYMBOL


(1) Addresses of Input Signals from Machine (\#1000-\#1063)

These are the address numbers + bit numbers (\# ) for input signals like, push buttons, limit switch, etc. from the machine operation panel, machine controller, etc. This section should be determined by the machine tool builder.
(a) 1 bit of the address $\# 1000$ corresponds to 1 point of the input signal.
(b) The address number and the bit number are determined depending on the number of the pin and the number of the connector of the I/O board to which the input signal is connected.

Example:


Refer to the I/O lists shown in Appendix 1, 2 for details.
(c) The input signals in the order of \#10001999 are expressed by the following symbols.


### 5.2 ADDRESS MAP AND DISPLAY SYMBOL

 (Cont'd)
## (2) Addresses of Output Signais to Machine (\#1100-\#1162)

These are the address numbers + bit numbers (\# undin) of output signals like, lamp, solenoid, etc. from the machine operation panel, machine controller, etc. This section should also be decided through the machine tool builder.
(a) 1 bit of the address \#1100 corresponds to 1 point of the output signal.
(b) The address number and the bit number are determined, depending on the number of the pin and the number of the connector of the I/O board to which the input signal is connected.

Example:


Refer to the I/O Lists shown in Appendix 1 , 2 for details.
(c) The output signals in the order of \#1100\#1199 are expressed by the following symbols.

(No Contact) (NC Contact)
(3) Addresses (\#1200 - \#1295) of Input Signals from NC Main Section
In other words, these can be termed as output signals to the PC from the NC main section. For example, the address numbers + bit numbers with respect to the $M-B C D$ signals. These numbers in the order of \#1200 are determined as standard signals and they can not be changed.
(a) 1 bit of addresses between \#1200 and \#1295 corresponds to 1 point of the input signal.

Example:


M function BCD output

Refer to "Appendix : I/O list" for details.
However, they differ for YASNAC J50L (for lathes) and YASNAC J50M (for machining centers).
So, refer to the corresponding list.
(b) The input signals in the order of \#1200\#1295 are expressed by the following symbols.

(a)

(b)
\#12
\#12

## (4) Addresses (\#1300 - \#1338) of Output Signals from NC Main Section

In other words, these can be termed as input signals to NC main section from the PC. For example, the address numbers and the bit numbers with respect to the EDIT and MEM (memory operation) selection.

The numbers between 1300 and 1329 are determined as standard signals and they can not be changed.
(a) l bit of the addresses between \#1300-\#1329 corresponds to 1 point of the input signal.

Example:


Refer to "Appendix: I/O list" for details. However, they differ for YASNAC J50L and YASNAC J50M. So, refer to the corresponding list.
(b) The output signals between \#1300 and \#1329 are expressed by the following symbols.

(5) Addresses (\#1400 - \#1999 except for \#1700 \#1799) for Internal Relays
These are the address numbers and bit numbers with respect to the internal relays which can only be used inside the PC while preparing the sequence program.
(a) l bit of the addresses between \#1400 - \#1492 corresponds to 1 internal relay, for example.

I/O list example:

(b) The number of usable internal relays are as follows.

500 bytes $\times 8$ bits $=4000$ relays
(c) The internal relay and its contact point are expressed by the following symbol.


There is no limit for NO and NC contact points until the program memory capacity is exceeded.
(d) Adressed used in register cannot be used as internal relay.
(6) Addresses (\#1400 - \#1999 except for \#1700 - \#1799) of Register
These are the address numbers with respect to the 1 byte ( $=8$ bits) register for general purpose use. These registers are used for register instruction or for the working addresses of macro instructions.
(a) 1 address number corresponds to 1 register of 1 byte.

I/O list example:

(b) Number of usable registers are as follows: 500 registers from \#1400 to \#1999 except for \#1700 tc \#1799.
(c) In a register, the address itself is the expression symbol. The following shows two examples of the symbols.

(d) Addresses used in internal relay cannot be used as register.
(7) Addresses of Timer (\#1700-\#1799)

These are the addresses with respect to the timers. They are used in the instruction of timers.
(a) 1 address number corresponds to 1 timer.

1/O list example:

(b) The time unit and the number of usable timers are shown in the following table.

| Address No. | No. of <br> timers | Time unit |
| :--- | :---: | :---: |
| $\# 1700-\# 1709, \# 1760-\# 1769$ | 20 | $1=8 \mathrm{msec}$ |
| $\# 1710-\# 1729, \# 1790-\# 1799$ | 30 | $1=100 \mathrm{msec}$ |
| $\# 1730-\# 1749, \# 1780-\# 1789$ | 30 | $1=50 \mathrm{msec}$ |
| $\# 1750-\# 1759$ | 10 | $1=1 \mathrm{sec}$ |
| $\# 1770-\# 1773$ | 4 | $1=1 \mathrm{~min}$ |

The range of set values is $0-255$. ( $0-127$ for variable timer.)
(c) The symbol example of timers is given below.

Example:


### 5.2 ADDRESS MAP AND DISPLAY SYMBOL (Cont'd)

(8) Battery Backed-up Memory (\#7000-\#7999)
(a) The above addresses of \#7000 to \#7295 are differentiated from others by the name "battery backed-up memory." That means, the data of \#7000 to \#7295 are preserved in the battery back-up memory in the standard NC main section. So, even if the power supply is turned off, the data are not erased.
(b) The sequence program of PC unit can only handle image data of the PC unit. The original data from NC main section can not be handled (reading or writing).
(c) Following 3 types of battery backed-up memory data are available.
Sequencer parameter: \#7000 - \#7099
$\left.\begin{array}{l}\text { Keep relay: } \\ \text { Keep memory: }\end{array}\right\}$ \#7100-\#7999

(d) Transfer to sequencer parameter data to PC In addition to the power supply turning on, the sequencer parameter data is transferred to PC from the NC main unit under the following conditions. Through the parameter writing operation, even if a single sequencer parameter data is modified, then all the sequencer parameter data are transferred. Consequently, all the image data of the PC are always latest data. The sequencer parameter data can only be read in the sequence program and they must not be modified.
(e) Transfer of keep relay and keep memory data to NC .
The image data of the PC unit keep relay and keep memory are sometimes read and written, so they are changed in the sequence program. Consequently, it becomes necessary to preserve the latest image data of the PC unit by transferring them to the battery backed-up memory as latest original data. And this procedure is explained below.

## Automatic data transfer

When the power supply of the unit is kept turned on, the data of \#7100- \#7999 get transferred from PC to NC unit.

## (9) Addresses (\#7000 - \#7099) of Sequencer Parameter

These are the address numbers corresponding to the parameter of the sequencer. The data of \#7000 - \#7099 can be changed through the normal writing operation. These data can be used in a sequence program in the following two procedures: (a) Using as 1 bit data and (b) Using as 1 byte data.
(a) Using as 1 bit data

I/O list example:


Symbol expression is carried out in the following way.

(b) Using as 1 byte data

I/O list example:


The symbol expression is the address number. The example of using in timer is shown in the following figure.

Example:

(10) Addresses (\#7100 - \#7999) of Keep Relay

These are the address numbers and bit numbers of the keep relays used in the PC.
(a) l bit of \#7100-\#7999 corresponds to 1 keep relay.

I/O list example:

(b) The number of usable keep relays is as follows.

900 bytes $\times 8$ bits $=7200$
(c) The keep relays and their contact points are
expressed by the following symbols.


## (11) Addresses (\#7100 - \#7999)

These are the addresses corresponding to the 1 byte memory which can be preserved even after turning off the power supply. If the performance is limited only to the preservation of data, the keep memory can be used in the same way as that of a register. Consequently, the keep memory can also be used as an object of register instruction or as supplementary data of macro instruction. Especially, when preparing a sequence program for memory random type ATC, this keep memory becomes necessary.
(a) 1 address number beyond \#7100 corresponds to one keep memory of 1 byte ( 8 bits).

I/O list example:

(b) The number of usable keep memory is as follows:
900 memories from \#7100 to \#7999
(c) The address number itself stands for the symbol of the keep memory.


MOV: Transfer the contents of register \#1500 to keep memory \#71
(12) Writing Initial Values of Keep Relays and Keep Memories

When preparing a sequence program by using the keep relays and keep memories, it becomes necessary to set the initial values prior to the execution.
(a) Set the system number switch of NC unit at " 1 " and then turn on the power supply.
(b) Depress the DGN function key.

Input/output signal ON/OFF state will be displayed on the CRT screen:
(c) After keying-in in the order of $17 \square 1$, if the cursor key is depressed, then the following display will be obtained.


### 5.2 ADDRESS MAP AND DISPLAY SYMBOL

## (Cont'd)

(d) Adjust addresses \#7105 to \#7294 for initial condition setting by depressing the cursor.
(e) If the INSRT (insert) key is depressed, the cursor will move in the right hand direction, and will move to the 7 th bit position of the address.
(f) Keep on pressing the cursor key until it becomes adjusted to the position of the decimal display.
(g) Key-in the desired values ( $0-255$ ) for setting initial condition and then depress the $W R$ key. The decimal display will get changed to the presently keyed in value.
(h) If the INSRT key is depressed, the cursor will move to the left hand position \#. Thereby, the setting of one address number is completed.
(i) Repeat steps (d) to (h) to write all the desired initial values of the address numbers.
(j) Adjust the system number switch to "0."

Note: If a particular bit is desired to be changed $0 \underset{\rightleftarrows}{ }$, carry out following operations after the operation of item 5). Depress the cursor key and adjust the cursor to the bit desired to be changed, then depress $W R$ key.
$0 \rightleftarrows 1$ change will be obtained.
$1 \rightleftarrows 0$ change will be obtained if the $W R$ key
is depressed again.
(13) Writing of Keep Relay Numerical Input (Optional only for J50M)
Writing to keep memory (\#7100-\#7999) can be normally executed from 0 to 255 , however, 4 -digit writing is also possible with numbers \#8600 \#8999. \#7100 - \#7499 and \#8600-\#8999 correspond to each other as shown in the figure below. \#7101 is altered by writing and alteration of $\# 8601$.

Note: When keep memory is referred from sequence, use \#7100-\#7499, not \#8600 - \#8999.

(a) Keep memory display

Following displays are added to existing \#7100 \#7499 display:
Depress function key DGN.
Key-in 8, 6, 0,, 1 and depress cursor $\square$. CRT screen has display as shown in either Fig. (i) or (ii).
[Hereafter Fig. (i) is to be called 2-digit display, while Fig. (ii) is to be called 4-digit display.]


Fig. (i) \#6022 D2=0 \#6355=8602 \#6356=8604


Fig. (ii) \#6022 D2=1 \#6355=8602 \#6356=8604

For Fig. (ii), even and uneven number keep memories are used in pairs, 0 to 9999 are available by expressing the higher 2 digits of the decimal 4 digits with even No. keep memory, and lower 2 digits with uneven No. keep memory.

Pot No. display [Figs. (i), (ii)]
When the max. and min. keep memory numbers are set to parameters \#6355 and \#6356, Figs. (i) and (ii) show how \#6355 and \#6356 are set for \#7402 and \#8604, respectively.
(b) Writing to keep memory

Turn system No. switch to "l".
Use page cursor keys and to move the cursor to keep memory No. to be changed. Input new figure and depress WR key. Procedure mentioned above enables \#8600-\#8999 range data to be changed and set.

Notes:

- The same memory is used for \#8600-\#8999 and \#7100 - \#7499: if a value of \#8602 is changed, that of \#7102 is changed to the same value.
- When the display can be extended up to 9999 , as in Fig. (ii), the even number keep memory data are changed to one lower number and cursor moves there by writing when the cursor is at an uneven keep memory number.
- If \#6355 and \#6356 are set conversally, pot No. title and pot No. are not normally displayed. However, if \#6355 and \#6356 have keep memory No. on the same page, pot No. title is displayed. [Refer to Fig. (iii).]
- If uneven number is set by mistake for $\# 6355$ when 4 -digit display (\#6022 D2=1), pot No. is displayed from the even number keep memory No. which is one number higher than the pot No.


Fig. (iii) \#6022 D2=0 \#6355=8604 \#6356=8602

| DIAGNOSIS | O0000 N0000 |
| :--- | :---: |
| P-NO | T-NO |
| $\# 8600$ | 0201 |
| $\# 8601$ |  |
| $\# 8602$ | 0403 |
| $\# 8603$ |  |
| $\# 8604(001)$ | 0805 |
| $\# 8605$ | 0807 |
| $\# 8606(002)$ |  |
| 8609 |  |

Fig. (iv) \#6022 D2=1 \#6355=8603 \#6356=8606

- If a number lower than that for \#6355 is set for \#8600, pot number from \#8600 is lower than the number already set to display. [Refer to Fig. (v).]

| DIAGNOSIS | O0000 N0000 |
| :---: | :---: |
| P-NO | T-NO |
| $\# 8600(010)$ | 01 |
| $\# 8601(011)$ | 02 |
| $\# 8602(012)$ | 03 |

Fig. (v) \#6022 D2 $=0$ \#6355=7391

- When pot number is not displayed, set 0 for \#6355 and \#6356.
- In 2 -digit display (\#6022 D2=0), writing-in more than a 3 -digit number is not accepted.
(14) Address Setting of $1 / 0$ Board

I/O board has a rotary switch for address setting. For rotary switch and address, refer to the table below.
(a) Standard I/O Board

| FC861 |  |  | FC810/FC860 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1/O Area No. | Input | Output | I/O Area No. | Input | Output |
| 1-1 | $\begin{gathered} \# 1000 \\ \text { to } \\ \# 1007 \end{gathered}$ | $\begin{aligned} & \text { \#1100 } \\ & \text { to } \\ & \text { \#1106 } \end{aligned}$ | 1 | $\begin{aligned} & \text { \#1000 } \\ & \text { to } \\ & \# 1013 \end{aligned}$ | $\begin{aligned} & \text { \#1100 } \\ & \text { to } \\ & \text { \#1111 } \end{aligned}$ |
| 1-2 | $\begin{aligned} & \text { \#1008 } \\ & \text { to } \\ & \# 1015 \end{aligned}$ | $\begin{aligned} & \text { \#1108 } \\ & \text { to } \\ & \# 1114 \end{aligned}$ |  |  |  |
| 2-1 | $\begin{aligned} & \text { \#1016 } \\ & \text { to } \\ & \text { 1023 } \end{aligned}$ | $\begin{gathered} \text { \#1116 } \\ \text { to } \\ \text { \#1122 } \end{gathered}$ | 2 | $\begin{gathered} \text { \#1016 } \\ \text { to } \\ \text { \#1029 } \end{gathered}$ | $\begin{aligned} & \text { \#1116 } \\ & \text { to } \\ & \text { \#1127 } \end{aligned}$ |
| 2-2 | $\begin{gathered} \text { \#1024 } \\ \text { to } \\ \text { 11031 } \end{gathered}$ | $\begin{gathered} \text { \#1124 } \\ \text { to } \\ \# 1130 \end{gathered}$ |  |  |  |
| 3-1 | $\begin{gathered} \# 1032 \\ \text { to } \\ \# 1039 \\ \hline \end{gathered}$ | $\begin{gathered} \text { \#1132 } \\ \text { to } \\ \# 1138 \end{gathered}$ | 3 | $\begin{gathered} \text { \#1032 } \\ \text { to } \\ \# 1045 \end{gathered}$ | $\begin{aligned} & \text { \#1132 } \\ & \text { to } \\ & \text { \#1143 } \end{aligned}$ |
| 3-2 | $\begin{aligned} & \text { \#1040 } \\ & \text { to } \\ & \# 1047 \end{aligned}$ | $\begin{array}{r} \# 1140 \\ \text { to } \\ \# 1146 \\ \hline \end{array}$ |  |  |  |
| 4-1 | $\begin{gathered} \text { \#1048 } \\ \text { to } \\ \# 1055 \end{gathered}$ | $\begin{gathered} \# 1148 \\ \text { to } \\ \# 1154 \\ \hline \end{gathered}$ | 4 | $\begin{gathered} \# 1048 \\ \text { to } \\ \# 1061 \end{gathered}$ | $\begin{aligned} & \text { \#1148 } \\ & \text { to } \\ & \text { \#1159 } \end{aligned}$ |
| 4-2 | $\begin{gathered} \# 1056 \\ \text { to } \\ \# 1063 \end{gathered}$ | $\begin{aligned} & \text { \#1156 } \\ & \text { to } \\ & \text { \#1162 } \end{aligned}$ |  |  |  |

For rotary switch (SW1) setting and I/O area No., refer to the table below.

| SW1 | I/O Area No. |  |  |
| :---: | :---: | :---: | :---: |
|  | FC861 | FC810/FC860 |  |
| 0 | No setting | No setting |  |
| 1 | $1-1$ | 1 |  |
| 2 | $1-2$ | No setting |  |
| 3 | $2-1$ | 2 |  |
| 4 | $2-2$ | No setting |  |
| 5 | $3-1$ | 3 |  |
| 6 | $3-2$ | No setting |  |
| 7 | $4-1$ | 4 |  |
| 8 | $4-2$ | No setting |  |
| 9 | $\quad$ No setting |  |  |
| to |  |  |  |
| $F$ |  |  |  |

### 5.2 ADDRESS MAP AND DISPLAY SYMBOL

(Cont'd)
(b) CRT Panel Built-in I/O Board

|  | SP50 |  |
| :---: | :---: | :---: |
| I/O Area No. | Input | Output |
| $1-1$ | \#1000 to \#1007 | \#1100 to \#1103 |
| $1-2$ | \#1008 to \#1015 | \#1108 to \#1111 |
| $2-1$ | \#1016 to \#1023 | \#1116 to \#1119 |
| $2-2$ | \#1024 to \#1031 | \#1124 to \#1127 |
| $3-1$ | \#1032 to \#1039 | \#1132 to \#1135 |
| $3-2$ | \#1040 to \#1047 | \#1140 to \#1143 |
| $4-1$ | \#1048 to \#1055 | \#1148 to \#1151 |
| $4-2$ | \#1056 to \#1063 | \#1156 to \#1159 |

For rotary switch (SWl) setting and I/O area No., refer to the table below.

| SW1 | I/O Area No. |
| :---: | :---: |
|  | SP50 |
| 0 | No setting |
| 1 | $1-1$ |
| 2 | $1-2$ |
| 3 | $2-1$ |
| 4 | $2-2$ |
| 5 | $3-1$ |
| 6 | $3-2$ |
| 7 | $4-1$ |
| 8 | $4-2$ |
| 9 |  |
| to | No setting |
| $F$ |  |

### 5.3 1/O LIST AND SEQUENCE LADDER

The data list of the address map is called the I/O lists. The I/O lists for J50L (for lathes) J50M (for machining centers) are shown in the Appendixes at the end of this manual.
(1) For preparing the sequence ladder, first of all, carry out the assignment of the I/O signals (\#1000 and \#1100) between the PC and the machine tool.
(2) After the completion of the assignment of the I/O signals, refer to the I/O list as a list for data and freely prepare sequence ladder through the command symbols of the PC. In this case, it is convenient to use the abbreviated names like SW7, SOL A, etc. for element names.
(3) Complete the assignment of the address numbers for each element: internal relay, register, timer, etc. for the completed and checked sequence ladder. Thereby, the complete sequence ladder and a complete I/O list is obtained.

## 6. SEQUENCE CONTROL METHOD

Sequence control through the $P C$ is carried out successively through the software, so the operations are quite different from that of the simultaneous processing in the case of normal relay circuit. So, it is necessary to have clear understanding of this point prior to programming.

### 6.1 DIFFERENCES IN OPERATION

Relay sequence: Each element is simultaneously processed with regard to time.
PC sequence: Each element is successively processed. The ladder is repeatedly processed at a constant period. This period is called scanning time. (Scanning time Ex.: 8 msec $\times \mathrm{n}$ times)

Example:


The above PC sequence ladder is operated in the following sequence. Simultaneous processing is never carried out.
(1) Condition of contact point $A$ is read.
(2) This is output to internal relay $B$ as it is.
(3) Condition of contact point $A$ is read.
(4) AND logic is taken from the NC contact point of relay $B$.
(5) The result is output to internal relay $D$.

Due to this successive processing, the internal relay $D$ is not turned on. On the other hand, if the above ladder is executed by the relay sequence, the relay $D$ is turned on for a moment and thereby one shot operation is being carried out. As discussed above, it should always be remembered that the processing in the PC is carried out successively and then programming should be completed. For reference, if the above mentioned PC sequence ladder is coded according to PC command words, it takes the following form.


Example of coded sequence program (called list)

### 6.2 SCANNING TIME (PROCESSING TIME)

The execution time from the start to the end of a sequence program is called the scanning time. The scanning time for this PC is as follows.

High speed scanning time: 8 msec
Low speed scanning time: $8 \mathrm{msec} \times \mathrm{n}$
That means, in this PC, the sequence program can be processed by dividing it into the high speed processing part and the low speed processing part. In this case, write the program as follows.


The first part of the write sequence program needs high speed processing.
(1) Relationship between High Speed Processing and Low Speed Processing


### 6.2 SCANNING TIME (PROCESSING TIME) (Cont'd)

(a) From the beginning of the sequence to the RTH command, the high speed sequence program (high speed Seq.), as shown in the above figure, is surely executed once within 8 msec . During the execution of this high speed sequence, the input condition does not change.
(b) The low speed sequence program (low speed Seq.) after RTH command is divided into "n" items and one of them is executed in the remaining time of 8 msec . That means, the whole low speed sequence program is executed in $8 \mathrm{msec} \times$ " $n$ " times time. Consequently, the value of " $n$ " depends on the capacity of the whole program and the length of the high speed sequence program. Since the low speed program is divided into many parts, so the I/O condition changes in the middle. So, be sure to take NOTE of item 3 of this section.
(c) At the first part of the 8 msec section, all the input conditions (\#1000 and \#1200) are taken in the PC at a time.
(d) At the last part of 8 msec section, all the output conditions (\#1100 and \#1300) are output at a time.

## (2) Precautions for High Speed Processing Sequence Program

In this program, only the portion where high speed responses such as counting of ON/OFF are necessary, is handled. So limit it to the least possible size of the sequence program. Limit it within 100 steps when converted into contact point instruction.

## (3) Precautions for Low Speed Processing Sequence Program

(a) The scanning time for low speed processing differs depending on the capacity of the total sequence program ( $8 \mathrm{msec} \times$ " $n$ "). (The amount of program that can be executed within 8 msec is approximately 3000 steps when converted into contact point instruction. However, this amount of steps is the combination of high speed and low speed processings.)
(b) Since division processing is carried out during the execution of the low speed processing sequence program, the input condition changes. Consequently, all inputs to be used through the low speed processing sequence program need to be received through the internal relays at the top of the low speed processing sequence program. Then, use the contact point of the receiving relay in place of the input.


Through the above operations, the input conditions may be kept unchanged during l cycle of execution of the low speed processing sequence program.
(c) If the output of the high speed processing sequence program is to be used in the low speed processing sequence program, the processing like (b) needs to be carried out.
(d) The output signals which are not desired to be output until the end of the execution of low speed processing sequence program, once received outputs them through the internal relays without outputting them to the addresses of output of the PC unit. Then, do not connect the same to the address of the external output at the tail of the low speed processing sequence program.


Write the desired output after one cycle of the low speed processing sequence

### 6.3 MEMORY CAPACITY OF SEQUENCE PROGRAM

The sequence program is finally written to the EPROM (Erasable Program Rom) and then used.
The capacity of the program memory of this PC can be used according to the following distribution.

| Division | No. of Bytes | Step <br> Convension | No. of <br> PROMs | PROM Location <br> on PC Board <br> JANCD-CP50 |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 32 k bytes | Approx. <br> 8000 steps | 1 | 30 |

(Usually, relay instruction is of $3-7$ bytes and other commands are of $1-25$ bytes range.) For the memory storing the sequence program of 16 K bytes, 4000 steps ( $16 \mathrm{~K} / 4=4 \mathrm{~K}$ ( 4000 steps) is required, if approximately 4 bytes is used for one step.

## 7. PC INSTRUCTIONS

This chapter explains the 61 type basic instructions and 11 type macro instructions that can be used with this PC while describing their functions, display symbols and coded lists.

### 7.1 PRELIMINARY KNOWLEDGE

(Registers to store intermediate results during logical operation)
(1) $P C$ is provided with a register to store intermediate results of logical operation of sequence programs, and it consists of 1 bit +16 bits, as shown below.

(2) RR (Result Register)

1-bit register to which the result of operation currently executed is stored. The contact status (0 or 1) can be set into RR by the LD instruction or the RR contents can be output to the relay address by the OUT instruction. Also, l-bit shift of the stack register contents to RR (after operation) by the STR or AND-STR instruction is possible.
(3) Stack Register (Stack, ST0 - ST15)

Intermediate operation resulting from long logical operation can be saved into the stack register sequentially up to 16 bits.

Data in RR is shifted to ST0 by the STR or STR-NOT instruction, and data in the stack register is shifted by 1 bit toward right. Also data in ST0 and RR is operated by the ANDSTR or OR-STR instruction, set into RR, and data in the stack register is shifted by 1 bit toward left. ST15 is cleared to "0." If the number of STR or STR-NOT instructions does not equal to the number of AND-STR or OR-STR instructions used in a series of long logical operations until the final result is obtained, it results in an error. In other words, the number of times that data is saved in the stack and the number of times that data is fetched out must be equal.

### 7.2 TYPES OF INSTRUCTIONS AND LISTS

(1) Instruction Types

There are the following types in the instructions used with PC.

Basic instructions (61 types)

| (1) Instructions for relay: | 13 types |
| :--- | ---: |
| (2) Instructions for registers: | 37 types |
| (3) Instructions for timers: | 2 types |
| (4) Control instructions: | 7 types |
| Total | 59 types |

## Macro instructions

(1) Macro instructions:
9 types
(2) Auxiliary instructions:
4 types

### 7.2 TYPES OF INSTRUCTIONS AND LISTS (Cont'd)

(2) List of instructions for relay

| No. | Instruction | * | Meaning | RR after operation | Page |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | LD | 1 | Reads signal status ( 0 or 1) and sets it to RR | 1 | 19 |
| 2 | LD-NOT | 1 | Reads inversion signal status and sets it to RR. | 1 | 19 |
| 3 | AND | 1 | Sets AND of contact and RR to RR (AND). | 1 | 20 |
| 4 | AND-NOT | 1 | Sets AND of inversion signal and RR to RR (Reverse AND) | $1$ | 20 |
| 5 | OR | 1 | Sets OR of signal and RR to RR (OR). | 1 | 20 |
| 6 | OR-NOT | 1 | Sets OR of inversion signal and $R R$ to $R R$ (Reverse OR). | 1 | 20 |
| 7 | XOR | 1 | Sets uncoincidence between signal and $R R$ to $R R$. | 1 | 20 |
| 8 | XNR | 1 | Sets coincidence between signal and RR to RR. | 1 | 20 |
| 9 | STR | 1 | Loads RR contents to stack and executes LD instruction. | $i$ | 21 |
| 10 | STR-NOT | 1 | Loads RR contents to stack and executes LD NOT instruction. | $1$ | 21 |
| 11 | AND-STR | 1 | Sets AND of RR and stack to RR. | 1 | 21 |
| 12 | OR-STR | 1 | Sets OR of RR and stack to RR. | 1 | 21 |
| 13 | OUT | 1 | Writes operation results (RR) to relay (address) | - | 21 |

Note:

1. The * column shows the execution time converted to the contact instruction ( $1=$ One contact instruction)
2. The $\downarrow$ mark shows that the $R R$ contents change after instructions are operated. The - mark shows that no change occurs.
(3) List of Instructions for Timers

| No. | Instruction | $*$ | Meaning | RR after <br> operation | Page |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | TIM | 10 | Timer processing (Fixed timer) | time up $=1$ | 22 |
| 2 | TMR | 10 | Timer processing (Variable timer) | time up $=1$ | 22 |

(4) List of Instructions for Registers

| No. | Instruction | * | Meaning | RR after operation | Page |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | INR | 3 | Adds +1 to register contents. | - | 22 |
| 2 | DCR | 3 | Adds - 1 to register contents. | - | 22 |
| 3 | CLR | 2 | Clears the register contents. | - | 23 |
| 4 | CMR | 3 | Inverts the register contents. | - | 23 |
| 5 | ADI | 3 | Addition of register contents and numeric. | - | 23 |
| 6 | SBI | 3 | Subtraction of register contents and numeric. | - | 23 |
| 7 | ANI | 3 | AND of register contents and numeric. | - | 24 |
| 8 | ORI | 3 | OR of register contents and numeric. | - | 24 |
| 9 | XRI | 3 | XOR of register contents and numeric. | - | 24 |
| 10 | DEC | 3 | Coincidence of register contents and numeric. | 1 | 24 |
| 11 | COI | 4 | Coincidence of register contents and numeric. | 1 | 24 |
| 12 | CMP | 3 | Comparison of register contents and numeric. | 1 | 25 |
| 13 | CPI | 4 | Comparison of register contents and numeric. | 1 | 25 |
| 14 | MVI | 3 | Load numeric to a register. | - | 25 |
| 15 | ADD | 4 | Adds registers R1 and R2 and stores the result in R2. | - | 25 |
| 16 | SUB | 4 | Subtracts R1 from R2 and stores the result in $R 2$. | - | 25 |
| 17 | ANR | 4 | Takes AND of R1 and R2 and stores the result in R2. | - | 25 |
| 18 | ORR | 4 | Takes OR of R1 and R2 and stores the result in R 2 . | - | 25 |
| 19 | XRR | 4 | Takes XOR of R1 and R2 and stores the result in R2. | - | 26 |
| 20 | CPR | 5 | Checks the result of comparison of R1 with R2, and stores the result in R 2 . | , | 26 |
| 21 | COR | 5 | Checks coincidence between R1 and R2, and sets the result in RR. | 1 | 26 |
| 22 | MOV | 4 | Transfers R1 contents to R2. | - | 26 |
| 23 | DST | 5 | Transfers AND of R1 contents and numeric to R2. | - | 26 |
| 24 | DIN | 7 | Data extraction | - | 27 |
| 25 | ADC | 4 | Double length addition | 1 | 27 |


| No. | Instruction | * | Meaning | RR after operation | Page |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 26 | ADDW | 4 | Adds double length registers (WR2 and WR1) and stores the result in WR2. | - | 27 |
| 27 | SUBW | 4 | Subtracts WR1 from WR2 and stores the result in WR 2. | - | 28 |
| 28 | MULW | 10 | Multiplies double length register (WR2) with register (R1) and stores the result in WR2. | RR is set toR11 <br> when <br> overflow occurs. |  |
| 29 | DIVW | 15 | Divides double length register (WR2) by register (R1) and stores the result in WR2. | O | 28 |
| 30 | INRW | 3 | Adds +1 to double length register contents. | - | 29 |
| 31 | DCRW | 3 | Adds - 1 to double length register contents. | - | 29 |
| 32 | CLRW | 3 | Clears double length register contents. | - | 29 |
| 33 | CMRW | 2 | Inverts double length register contents. | - | 29 |
| 34 | CORW | 3 | Sets coincidence result of double length registers (WR2 and WR1) to RR. | 1 | 29 |
| 35 | CPRW | 3 | Sets comparison result of double length registers (WR2 and WR1) to RR. | 1 | 29 |
| 36 | MVIW | 3 | Loads numeric to double length register. | - | 29 |
| 37 | DSTW | 5 | Transfers AND of double length register (WR1) contents and numeric to double length register (WR2). | - | 30 |

(5)

List of Control Instructions

| No. | Instruction | $*$ | Meaning | RR after <br> operation | Page |
| :--- | :--- | :--- | :--- | :---: | :---: |
| 1 | NOP | 1 | No-operation. | - | 30 |
| 2 | MCR | 1 | Start of master control relay. | - | 30 |
| 3 | END | 1 | End of master control relay. | - | 30 |
| 4 | RET | 1 | Sequence program termination. | - | 30 |
| 5 | RTI | 1 | RR is set to "1" and RET instruction is executed. | - | 30 |
| 6 | SET | 1 | Sets RR to "l." | 1 | 31 |
| 7 | RTH | 1 | High speed processing sequence program <br> termination. | - | 31 |

(6) List of Macro Instructions

| No. | Instruction | * | Meaning | RR after Operation | Page |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | SUBP 005 | $\begin{gathered} \text { Approx. } \\ 100 \end{gathered}$ | Counter. | 1 | 31 |
| 2 | SUBP 006 |  | Rotation (for control of rotating object). | 1 | 33 |
| 3 | SUBP 007 |  | Code conversion. | 1 | 35 |
| 4 | SUBP 009 |  | Pattern clear. | 1 | 36 |
| 5 | SUBP 011 |  | Parity check. | 1 | 37 |
| 6 | SUBP 014 |  | Data conversion (Binary $=\mathrm{BCD}$ ). | 1 | 37 |
| 7 | SUBP 017 |  | Data search. | 1 | 38 |
| 8 | SUBP 018 |  | Index data move. | 1 | 38 |
| 9 | SUBP 023 |  | Message display (Option). | 1 | 39 |

(7) List of Auxiliary Macro Instructions

| Instruction | $*$ | Meaning | RR after <br> Operation | Page |
| :--- | :---: | :--- | :---: | :---: |
| IPSH | 2 | Designation of numeric used by SUBP. | - | 31 |
| APSH | 2 | Designation of address of register used <br> by SUBP. | - | 31 |
| PUSH | 2 | Designation of address of register used <br> by SUBP. | - | 31 |
| TPSH | 2 | Designation of Table No. of PC table used <br> by SUBP. | - | 31 |

### 7.3 INSTRUCTIONS FOR RELAYS

(1) LD (Load)

RR after operation\{RR $\ddagger$
(1) Format


Example: \#10100
\#14312
(2) Reads contact status (1 or 0 ) and sets the results to RR.
(3) Normally this instruction is applied to Contact A (-ト)

(2) LD-NOT (Load Not) $\mid$ RR $\mid$ i
(1) Format LD-NOT \# $\mathrm{x} \times \mathrm{x} \times \mathrm{x}$ Internal signal name

Example: \#10100
\#14321
(2) Read inversion contact status (1 or 0 ) and sets the result to $R R$.
(3) Normally this instruction is applied to Con$\operatorname{tact} B(\not \subset)$.


| LD-NOT | \#10010 |
| :--- | ---: |
| AND-NOT | \#14123 |
| OUT | \#11012 |

### 7.3 INSTRUCTIONS FOR RELAYS (Cont'd)

(3) AND
\{RRI\}
(1) Format


Internal signal name
(2) Takes AND of contact and RR and loads the result to RR (AND).


| LD | $\# 10012$ |
| :--- | ---: |
| AND | $\# 14352$ |
| AND | $\# 14132$ |
| OUT | $\# 14040$ |

(4) AND-NOT
\{RRI!
(1) Format

$$
\frac{\text { AND-NOT }}{\frac{\# \times \times \times x}{\text { Internal signal name }}}
$$

(2) Takes AND of inversion contact and RR and loads the result to RR (Reverse AND).


LD-NOT \#10012
AND-NOT \#14352
AND-NOT \#14132
OUT \#14040
(5) OR
\{RR $\ddagger$
(1) Format


Takes OR of contact point and RR and loads the result to $R R$ (OR).

(6) OR-NOT
(1) Format OR-NOT \# $\frac{\# \mathrm{x} \times \mathrm{x} \times}{\text { Internal signal name }}$
(2) Taken OR of inversion contact point and $R R$ and loads the result to RR (Reverse OR).


$$
\begin{array}{ll}
\text { LD-NOT } & \# 10012 \\
\text { OR-NOT } & \# 14352 \\
\text { OR-NOT } & \# 14132 \\
\text { OUT } & \# 14040
\end{array}
$$

(7) XOR (Exclusive OR) \{RR 1$\}$
(1) Format


Internal signal name
(2) Loads dissidence between contact and RR to RR.

$\left.\begin{array}{ll}\text { LD } & \# 10012 \\ \text { AND-NOT } & \# 14352 \\ \text { STR-NOT } & \# 10012 \\ \text { AND } & \text { \#14352 } \\ \text { OR-STR } & \\ \text { OUT } & \# 14040\end{array}\right\} \equiv\left\{\begin{array}{ll|l|l|l|}\text { LD } & \# 10012 \\ \text { XOR } & \# 14352 \\ \text { OUT } & \# 14040\end{array} \begin{array}{|l|l|l|}\hline & \text { A } & \text { B } \\ \hline 0 & 0 & 0 \\ \hline 1 & 0 & 1 \\ \hline 0 & 1 & 1 \\ \hline 1 & 1 & 0 \\ \hline\end{array}\right.$
(8) XNR (Exclusive NR)
\{RR才\}
(1) Format


Internal signal name
(2) Loads coincidence between contract and RR to RR.


| LD | \#10012 | $\} \equiv\{$ | \#10012 <br> \#14352 <br> \#14040 | A | в | c |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AND | \#14352 |  |  | 0 | 0 | 1 |  |
| STR-NOT | \#10012 |  |  | 1 | 0 | 0 |  |
| AND-NOT | \#14352 |  |  | 0 | 1 | 0 |  |
| OR-STR |  |  |  | 1 | 1 | 1 |  |

(9) STR (Store)
\{RR $\ddagger$
(1) Format

(2) Loads RR contents to stack.


Can use stacks up to 16.

Then, executes the $L D$ instructions.
(3) Normally, this instruction is used for signal of Contact A ( - - ) .


| LD | \#10012 |
| :--- | ---: |
| OR | \#14001 |
| STR | \#10013 |
| OR | \#14002 |
| AND-STR |  |
| OUT | \#14041 |

(10) STR-NOT (Store NOT) $\{R R \downarrow \mid$
(1) Format STR-NOT \# $\frac{\# \mathrm{x} \times \mathrm{x} \mathrm{x}}{1}$

Internal signal name
(2) Loads RR contents into stack and then executes the LD NOT instruction.


LD-NOT \#10012
OR-NOT \#14001
STR-NOT \#10013
OR-NOT \#14002
AND-STR
OUT \#14041
(11) AND-STR (AND-Store)
\{RRI\}
(1) Format AND-STR
(2) Executes AND of RR and stack (STO) and loads the result to $R R$. The stack shifts by one each toward left.


| LD | $\# 10012$ |
| :--- | :--- |
| OR | $\# 14001$ |
| STR-NOT | $\# 10013$ |
| OR-NOT | $\# 14002$ |
| AND-STR |  |
| OUT | $\# 14041$ |

(12) OR-STR (OR-Store)
$\{R R 1\}$
(1) Format OR-STR
(2) Executes OR of RR and stack (ST0) and loads the result to RR.


| LD | $\# 10012$ |
| :--- | ---: |
| AND | $\# 14001$ |
| STR | $\# 10013$ |
| AND | $\# 14002$ |
| OR-STR |  |
| OUT | $\# 14041$ |

(13) OUT
(RR-)
(1) Format


Internal signal name
(2) Writes operation result (RR) to relay.


| LD | $\# 10012$ |
| :--- | :--- |
| AND | $\# 14001$ |
| OUT | $\# 14041$ |

### 7.4 INSTRUCTIONS FOR TIMERS

(1) TIM (Fixed Timer) $\{$ RR time up $=1\}$
(1) Format TIM

(2) The timer counts up in the state that the ST contact is ON ( $R P=1$ ), and sets $T M$ on after the set time. In the state of the ST contact being OFF ( $\mathrm{RR}=0$ ), TM is cleared and the timer is reset.
(3) The timer set value is in the range of 0 255 (decimal notation). However, make sure to write this in a hexadecimal notation (NOTE 1). The CRT display is also in a hexadecimal notation.
(4) Five types of timers can be used.

| Address | Types | No. of <br> Timers |
| :---: | :---: | :---: |
| \#1700-\#1709, \#1760-\#1769 | Timer of $1=8 \mathrm{msec}$ | 20 |
| $\# 1710-\# 1729, \# 1790-\# 1799$ | Timer of $1=0.1 \mathrm{sec}$ | 30 |
| $\# 1730-\# 1749, \# 1780-\# 1789$ | Timer of $1=50 \mathrm{msec}$ | 30 |
| $\# 1750-\# 1759$ | Timer of $1=1 \mathrm{sec}$ | 10 |
| $\# 1770-\# 1773$ | Timer of $1=1 \mathrm{~min}$ | 4 |



Note:

1. A conversion table between decimal and hexadecimal notation is provided in Appendix 3 at the end.
2. The same address must not be used in fixed timer and variable timer, for normal operation cannot be guaranteed.
(2) TMR (Variable Timer) $\{R R$ time $u p=1\}$
(1) Format

(2) The timer counts up in the state of the ST contact being $O N(R R=1)$, and $T M$ is set on after the set time. When the ST contact is $O F F(R R=0), T M$ is cleared and the timer is reset.
(3) The timer set value is in the range of 0 255 (decimal notation).
(4) Set the aforementioned timer value through the NC keyboard in the procedures of "Parameter Write Operation." In this case, the write can be in a decimal notation, and the CRT display is also in a decimal notation.
(5) The same as with the TIM instruction, 5 types of timers can be used with TMR.


LD \#10012
TMR \#1705, \#7042
OUT \#14041

### 7.5 INSTRUCTIONS FOR REGISTERS

(1) INR (Increment Register) (RR -;
(1) Format INR

( register number)
(2) Adds +1 to the register contents when the ST contact is $O N$ ( $R R=1$ ). This instruction is not executed when the $S T$ contact is $O F F(R R=0)$.
(3) The ST contact must be made before the INR instruction.
(4) When the ST contact is $\mathrm{ON},+1$ is added to the register contents in every $8 \times$ " $n$ " msec.

(2) DCR (Decrement Register) \{RR-\}
(1) Format DCR $\begin{aligned} \# \mathrm{xx} \times \mathrm{x} \\ \# 1500-\# 1599\end{aligned}$
\#1800 - \#1899
\#1900 - \#1999
(register number)
(2) When the $S T$ contact is $O N(R R=1),-1$ is added to the register contents. This instruction is not executed when the ST contact is OFF ( $R R=0$ ). The $R R$ contents remain unchanged.
(3) The ST contact must be made before the DCR instruction.


LD \#10012 DCR \#1505
(4) When the ST contact is $\mathrm{ON},-1$ is added to the register contents in every 8 x " n " msec .
(3) CLR (Clea) \{RR-\}
(1) Format

$$
\begin{aligned}
& \text { CLR \# x x x x } \\
& \text { \#1500 - \#1599 } \\
& \text { \#1800-\#1899 } \\
& \text { \#1900 - \#1999 } \\
& \text { (register number) }
\end{aligned}
$$

(2) Clears the register contents when the ST contact is $O N(R R=1)$. This instruction is not executed when the contact is OFF ( $R R=0$ ). The $R R$ contents remain unchanged

(4) CMR (Complement Register) \{RR-\}
(1) Format

\#1800 - \#1899
\#1900- \#1999
(register number)
(2) Inverts the register contents when the ST0 contact is $O N(R R=1)$. This instruction is not executed when the contact is OFF ( $R R=$ $0)$. The RR contents remain unchanged.
(3) The ST contact must be made before the CMR instruction.

(4) The register contents are inverted in every 8 x " n " msec when the ST contact is ON.
(5) ADI (Added Immediate) \{RR-।
(1) Format

(2) Adds the register contents and numeric and loads the result to the register when the ST contact is $O N$ ( $R R=1$ ). This instruction is not executed when the contact is OFF ( $R=0$ ). The $R R$ contents remain unchanged.
(3) The ST contact must be made before the ADI instruction.
(4) The ADI instruction is executed in every $8 \times$ " $n$ " msec when the ST contact is ON.


LD \#10012
ADI \#1505, 10 H
(6) SBI (Subtract Immediate) \{RR-;
(1)

(2) Subtracts the register contents and numeric and loads the result to the register when the $S T$ contact is $O N(R R=1)$. If it is OFF, the instruction is not executed. The RR contents remain unchanged.
(3) The ST contact must be made before the SBI instruction.


LD \#10012
ADI \#1505, 20 H
(4) The SBI instruction is executed in every 8 x " n " msec when the ST contact is ON.

### 7.5 INSTRUCTIONS FOR REGISTERS (Cont'd)

(7) ANI (And Immediate) |RR-|
(1) Format

$$
\begin{aligned}
& \text { \#1800 - \#1899 } \\
& \text { \#1900- \#1999 } \\
& \text { (register number) }
\end{aligned}
$$

(2) AND of the register contents and numeric is taken and loaded in the register when the ST contact is $O N(R R=1)$. If the contact is $O F F(R R=0)$, the instruction is not executed. The $R R$ contents remain unchanged.
(3) The ST contact must be made before the ANI instruction


> LD $\# 10012$
> ANI $\# 1505,55 \mathrm{H}$

|  | $\mathrm{D}_{7}$ | $\mathrm{D}_{6}$ | $\mathrm{D}_{5}$ | $\mathrm{D}_{4}$ | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Register | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| Numeric | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| Result | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |

(8) ORI (Or Immediate) \{RR-\}
(1) Format

$$
\begin{aligned}
& \text { ORI \# } \frac{\mathrm{xx} x \mathrm{x}}{\mathrm{x}}, \frac{\mathrm{x} \times \mathrm{x}}{\text { Numeric }} \\
& \# 1500-\# 1599 \text { (hexadecimal) } \\
& \# 1800-\# 1899 \\
& \# 1900-\# 1999
\end{aligned}
$$

(2) OR of the register contents and numeric is taken and loaded in the register when the $S T$ contact is $O N(R R=1)$. If the contact is $\operatorname{OFF}(R R=0)$, the instruction is not executed. The RR contents remain unchanged.

|  | $\mathrm{D}_{7}$ | $\mathrm{D}_{6}$ | $\mathrm{D}_{5}$ | $\mathrm{D}_{4}$ | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Register | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| Numeric | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| Result | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |

(9) XRI (Exclusive or Immediate)
(1) Everything is the same as in the ORI instruction, with an exception of the following table.

|  |  |  |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\mathrm{D}_{7}$ | $\mathrm{D}_{6}$ | $\mathrm{D}_{5}$ | $\mathrm{D}_{4}$ | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| Register | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| Numeric | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| Result | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 0 |

(10) DEC (Decode) \{RR $\mid$
(1) Format


Register and contact set
(2) $R R$ is one when the data and numeric of the 8 bits of the register and contact set are equal. This will occur irrelevant to $R R$ of the input side.
(3) No contact can be added before the DEC instruction. Use the COI instruction when a contact must be added.


$$
\begin{aligned}
& \text { DEC } \# 1505,10 \mathrm{H} \\
& \text { OUT } \# 14020
\end{aligned}
$$

(4) For example, if the $M$ function output is \#1222, to set on/off M11 with an M1l signal, the following must be given.

```
DEC #1222, OBH
OUT #l4100 (relay for M11)
```

(11) COI (Coincide Immediate) $|R R \ddagger|$ Format

(2) RR is set to "l" when the data and numeric of the register or contact set coincide when the $S T$ contact is $O N(R R=1)$. If the contact is OFF $(R R=0)$, the COI instruction is not executed. $R R$ is cleared.

(12) CMP (Compare) \{RR $\ddagger$
(1) Format


Register and contact set
(2) If the comparison result of the 8 -bit data and numeric of the register and contact set is that the register (contact set) is equal or greater than the numeric, RR is set to "1." If the register (contact set) is smaller than the numeric, $R R$ is cleared. This is executed irrelevant to $R R$ of the input side.
(3) No contact can be added before the CMP instruction. Use the CPI instruction when a contact must be added.

(13) CPI (Compare Immediate) $\{R R \uparrow!$
(1) Format


Register and contact set
(2) $R R$ is set to "1" if the comparison result of the data and numeric of the register or contact set is that the register (contact set) is greater or equal to the numeric when the ST contact is $O N$ ( $R R=1$ ). When the ST contact is OFF ( $R R=1$ ), the CPI instruction is not executed. $R R$ is cleared.


| LD | $\# 14002$ |
| :--- | :--- |
| CPI | $\# 1230,10 \mathrm{H}$ |
| OUT | $\# 14500$ |

(14) MVI (Move Immediate) |RR-|

(2) This instruction transfers the numeric to the register when the ST contact is ON ( $R R=1$ ). If the contact is OFF $(R R=0)$, the MVI instruction is not executed.

(3) $R R$ is not affected by the MVI instruction.
(4) If the ST contact is ON, the MVI instruction is executed in every $8 \times \mathrm{n}$ " msec.
(15) ADD (ADD Register) \{RR-\}
(1) Format

(2) When the $S T$ contact is $O N(R R=1)$, the register (R2) contents and register (R1) are added and the result is loaded in register ( $R 2$ ). The $R 1$ register contents remain unchanged. The $R R$ contents also remain unchanged. The ADD instructions not executed when the ST contact is OFF ( $R R=0$ ).


Note: In ADD or SUB, detection of overflow or underflow is not performed. With ADD, make the result less than 255 (FFH); with SUB, do not make $\mathrm{R}_{1}>\mathrm{R}_{2}$.
(16) SUB (Sub Register) (RR-)
(1) Everything is the same as the ADD instruction, except here the operation is subtraction ( $\mathrm{R} 2-\mathrm{R} 1 \rightarrow \mathrm{R} 2$ ).
(17) ANR (And Register) (RR-1
(1) Everything is the same as the ADD instruction, except here the operation is AND, (R2 AND R1 $\rightarrow$ R2)
(18) ORR (Or Register) (RR-)
(1) Everything is the same as the $A D D$ instruction, except here the operation is OR. (R2 OR R1 $\rightarrow$ R2)

### 7.5 INSTRUCTIONS FOR REGISTERS (Cont'd)

(19) XRR (Excluse or Register) (RR-1
(1) Everything is the same as the ADD instruction, except here the operation is XOR. (R2 XOR R1 $\rightarrow$ R2)
(20) CPR (Compare Register) \{RR $\}$
(1) Format

$$
\text { CPR } \frac{\# x \times x \times, \# x \times x \times x}{}
$$

(2) When the $S T$ contact is $O N$ ( $R R=1$ ), the difference between R1 and R2 is taken, and;
$R R$ is cleared if $R$ is smaller than $R 2$, and RR is set to "l" if R1 is greater than or equal to R2.

CPR is not executed when the ST contact is $O F F(R R=0)$. The $R R$ contents remain unchanged.

\#1501 < \#1502 ... Z1 is set. \#1501 $\geqq$ \#1502 . . . Z1 is cleared.
(3) The data in $R 1$ and $R 2$ remain unchanged when the CPR instruction is executed.

Note: The instructions for registers described in (16) through (20) execute their commands by $8 \times$ nms when the ST contact is on. The instructions $A D D$, $S U B$ and $X R R$ will change their register contents by $8 \times \mathrm{nms}$.
(21) COR (Coincide Register) \{RR \}
(1) Format

(2) When the $S T$ contact is $O N(R R=1)$ :

If $R 1$ is equal to $R 2, Z l$ is set.
If R1 is not equal to R2, Zl is cleared.
When the ST contact is OFF ( $R \mathrm{R}=0$ ), the COR instruction is not executed, and the $R R$ contents remain unchanged.


$$
\begin{aligned}
& \# 1501=\# 1502 \cdots \mathrm{Z1} \text { is set. } \\
& \# 1501=\# 1502 \cdots \mathrm{Zl} \text { is cleared. }
\end{aligned}
$$

(3) The data of $R 1$ and $R 2$ remains unchanged when the COR instruction is executed.
(22) MOV (Move Register) (RR-1
(1) Format

(2) The Rl register contents are transferred to Register R2 when the ST contact is ON ( $R R=1$ ). The Register $R 1$ contents remain unchanged.


$$
\begin{array}{ll}
\text { LD } & \# 14012 \\
\text { MOV } & \# 1501, \# 1502
\end{array}
$$

(3) $R R$ is not affected by the MOV instruction.
(23) DST (Data Store) (RR-\}
(1) Format DST

(2) When the $S T$ contacts in $O N$ ( $R R=1$ );

Register Rl and the numeric are ANDed, and the result is transferred to $R 2$. Register R1 remains unchanged. When the $S T$ contact is $\operatorname{OFF}(R R=0)$; The DST instruction is not executed.


$$
\begin{aligned}
& \mathrm{LD} \\
& \mathrm{DST} \\
& \text { \#15012, \#1502, OFH }
\end{aligned}
$$

|  | $\mathrm{D}_{7}$ | $\mathrm{D}_{6}$ | $\mathrm{D}_{5}$ | $\mathrm{D}_{4}$ | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reg. R1 | B | B | B | B | B | B | B | B |
| Numeric | 0 | 0 | C | 0 | 1 | 1 | 1 | 1 |
| Reg. R2 | 0 | 0 | 0 | 0 | B | B | B | B |

B: "1" or "0"
(3) RR is not affected by execution of the DST instruction.
(24) DIN (Data Insert) \{RR-|
(1)

Register or contact set (RI)
(2) When the $S T$ contact is $O N(R R=1)$, the Rl data and numeric are ANDed and the result is ORed with the AND of the R2 data and the numeric complement. The result is stored in R2 (data extraction). When the ST contact is OFF ( $R R=1$ ), the DIN instruction is not executed.


$$
\begin{aligned}
& \text { LD \#14012 } \\
& \text { DIN \#1501, \#1502, OFH }
\end{aligned}
$$

|  | $\mathrm{D}_{7}$ | $\mathrm{D}_{6}$ | $\mathrm{D}_{5}$ | $\mathrm{D}_{4}$ | $\mathrm{D}_{3}$ | $\mathrm{D}_{2}$ | $\mathrm{D}_{1}$ | $\mathrm{D}_{0}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| R 1 | A | A | A | A | A | A | A | A |
| R 2 | B | B | B | B | B | B | B | B |
| n | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| Result | B | B | B | B | A | A | A | A |

A, B: Data is "l" or "0."
(25) ADC (Add with Carry) |RR|
(1) Format $\operatorname{ADC} \frac{\# \mathbf{x} \mathbf{x} \mathbf{x} \mathbf{x}, \# \mathbf{x} \mathbf{x} \mathbf{x} \mathbf{x}}{}$
(2) Register R1, R2 and RR are added, and the result stored in Register R2. RR is set to "l" when a carry occurs.


LD NOT \#10012
ADC \#1501, \#1502
ADC \#1500, \#1503

(3) RR must be cleared to execute the ADC instruction.
(26) ADDW (Add Word Register) (RR-\}
(1)

Format ADD


Low side of double length register (WR1)
(2) When the $S T$ contact is $O N(R R=1)$, the contents of double length registers, WR2 and WRl, are added and the result is stored in WR2. WR1 remains unchanged. $(W R 2)+(W R 1) \rightarrow(W R 2)$. The RR contents do not change by the operation. When the ST contact is OFF ( $R R=0$ ), the ADDW instruction is not executed. The numeric is judged without code.

(WR2)

(WR1)

(WR2)

### 7.5 INSTRUCTIONS FOR REGISTERS (Cont'd)

(27) SUBW (Sub Word Register) (RR-1
(1)

Format
SUBW


Low side of double length register (WR2)
Low side of double length register (WR1)
(2) When the $S T$ contact is $O N(R R=1)$, the results of the contents of double length registers, WR2 minus WR1 is stored in WR2. WR1 remains unchanged.
(WR2) - (WR1) $\rightarrow$ (WR2)
When the ST contact is OFF $(R R=0)$, the SUBW instruction is not executed. The numeric is judged without code.

(WR2)

| \#1503 | \#1502 |
| :--- | :--- |

(WR1)

(WR2)
(28) MULW (Mul Word Register) \{RR $\ddagger$
(1) Format MULW \# $\mathrm{x} \times \mathrm{x} \times \mathrm{x}$ \# $\mathrm{x} \times \mathrm{x} \times$ Register (R1) $\begin{aligned} & \text { Low side of } \\ & \text { double length } \\ & \text { register (WR2) }\end{aligned}$
(2) When the $S T$ contact is $O N(R R=1)$, the contents of double length register, WR2 and register Rl are multiplied, and the result is stored in WR2. R1 remains unchanged.
$(W R 2) \times(R 1) \rightarrow(W R 2)$
When the $S T$ contact is $O F F(R R=0)$, the MUL instruction is not executed. The numeric is judged without code. If the result is overflown, more than "FFFFH," $R R$ equals one.


LD \#14012 MUL \#1500, \#1502
(WR2)

(R1)

(WR2)
(29) DIVW (Division Word Register) \{RR-\}
(1) Format

(2) When the $S T$ contact is $O N(R R=1)$, the contents of double length register WR2 is divided by register Rl and the result is stored in WR2. WR1 remains unchanged. When the ST contact is OFF ( $\mathrm{RR}=0$ ), DIV instruction is not executed. The numeric is judged without code. If WR1 is " 0 ," operation will not be executed.


$$
\begin{aligned}
& \text { LD } \# 14012 \\
& \text { DIV } \# 1500, \# 1502
\end{aligned}
$$

(WR2)

(R1)

(WR2)
(30) INRW (Increment Word Register) |RR- |
(1) Format


Low side of double length register
(2) When the $S T$ contact is $O N,+1$ is added to the double length register contents.


$$
\begin{array}{ll}
\text { LD } & \# 14012 \\
\text { INRW } & \# 1500
\end{array}
$$

(31) DCRW (Decrement Word Register) (RR-1
(1) The same as INRW, but the operation here is addition of -1 to the double length register contents.
(32) CLRW (Clear Word Register) (RR-)
(1) The same as INRW, but here the double length register contents are cleared.
(33) CMRW (Complement Word Register). \{RR-\}
(1) The same as INRW, but here the double length register contents are inverted.
(34) CORW (Coincide Word Register) $\{\operatorname{RR} \uparrow \mid$
(1) Format

(2) When the ST contact is ON ( $R \mathrm{R}=1$ ), WRI and WR2 are checked for the coincidence;

If $W R 1$ and $W R 2$ are equal, $R R$ is set to 1. If $W R 1$ and $W R 2$ are not equal, $R R$ is cleared.
When the ST contact is OFF ( $\mathrm{RR}=0$ ), the CORW instruction is not executed, and the RR contents remain unchanged.


$$
\begin{array}{ll}
\text { LD } & \# 14012 \\
\text { CORW } & \# 1500, \# 1502 \\
\text { OUT } & \# 14123
\end{array}
$$

[^1](3) The data of WR1 and WR2 do not change when the CORW instruction is executed.
(35) CPRW (Compare Word Register) $\langle R R \ddagger$;
(1) Format


Double length register (WR1)
(2) When the $S T$ contact is $O N(R R=1)$, WR1 and WR2 are checked for the difference;

If WR1 is smaller than WR2, RR is cleared. If $W R 1$ is greater than or equal to WR2, $R \mathrm{R}$ is set.

When the $S T$ contact is $O F F(R R=0)$, the CPRW instruction is not executed. The RR contents remain unchanged.


> LD \#14012
> CPRW \#1500, \#1502
> OUT \#14123
\#1500 < \#1502 ... Z1 is set.
$\# 1500 \geqq \# 1502 \cdots \mathrm{Z}$ is cleared.
(36) MVIW (Move Immediate Word Register) (RR-1
(1)

Format MVIW


Numeric
(High side)
Double length register
(2) When the $S T$ contact is $O N(R R=1)$, the numeric is transferred to the register. When the ST contact is OFF $(R R=0)$, the MVIW instruction is not executed.


The RR contents are not affected by execution of the MVIW instruction.

### 7.5 INSTRUCTIONS FOR REGISTERS (Cont'd)

(37) DSTW (Data Store Word Register) (RR-)
(1) Format DSTW $\frac{\# x \times x x,}{\# x \times x x} \frac{\text { xxxxH }}{1}$

Register (WR1)
(2) When the ST contact is $\mathrm{ON}(\mathrm{RR}=1)$, Register $W R 1$ and the numeric and ANDed and the result is transferred to Register WR2. The WRI contents remain unchanged. When the ST contact is OFF ( $\mathrm{RR}=0$ ), the DSTW instruction is not executed.

$\begin{array}{ll}\text { LD } & \text { \#14012 } \\ \text { DSTW } & \# 1500, \# 1502, \text { OFOFH }\end{array}$

(3) The RR contents remain unchanged when the DS'T instruction is executed.

### 7.6 CONTROL INSTRUCTIONS

(1) NOP (No Operation) $\{$ RR-\}
(1) Format NOP
(2) No operation is conducted and the system moves to the next step. The RR contents remain unchanged.
(2) MCR (Master Control) \{RR-\}
(1) Format MCR
(2) When the Xl and X 2 contacts are $\mathrm{ON}(\mathrm{RR}=$ 1), the sequence ladder is released. When the X 1 and X 2 contacts are $O F F(R R=$ 0 ), the ladder up to END is executed in the state of RR being "0."

(3) Another MCR instruction can be given between MCR and END ( 7 levels max).
(4) When a timer instruction is included in MCR, the timer is cleared when MCR is OFF.
(5) Even if a self-holding circuit is formed between MCR and END instructions, the circuit output is OFF when MCR input contact is OFF.
(3) END (Master Control End) \{RR-\}
(1) Format END
(2) Indicates that $M C R$ is at the end.
(4) RET (Return) :RR- \}
(1) Format RET
(2) Indicate the end of sequence program.
(5) RTI (Return Indirect) \{RR-\}
(1) Format RTI
(2) When the ST contact is OFF, ladder of the next step is executed.

(6) SET (Set Return Register) \{RR-\}
(1) Format SET
(2) Forcibly sets RR to "1."
(7) RTH (Return High Sequence) : RR-;
(1) Format RTH
(2) Indicates the end of a high speed sequence program.

### 7.7 MACRO INSTRUCTIONS

Macro instructions (SUBPxxx) are provided to enable the operators to simply arrange operations of machine tools with which ladders cannot be prepared easily with basic instructions (relay instruction, register instruction, etc.) only. The following explains further details. The format of macro instructions is as follows:


The following auxiliary instructions are used with macro instructions:

- IPSH (Immediate Push) \{RR-\}
(1) Format IPSH $\frac{\mathbf{x} \times \times \mathrm{H}}{\text { Numeric (hexadecimal) }}$
(2) Directly designate the numeric used with SUBP.
- APSH (Address Push) \{RR- \}
(1) Format

(2) Designate the address of the register used with SUBP.
- PUSH (Push) \{RR-\}
(1) Format

(2) Designate the address where the numeric used with SUBP is stored.

TPSH (Table Push)
\{RR- \}Format $\operatorname{TPSH} \frac{\mathbf{x} \mathbf{x} \mathbf{x}}{\text { Table number }}$
(2) Designates the table number of $P C$ table used with SUBP.
(1) SUBP 005 (Counter)
(1) Function: This counter can be used in many ways to control machine tool operation according to the applications, as described below.
(a) Ring counter

This counter is ring counter. Accordingly, it returns to the initial value when a count signal is input after counting up to the preset value.
(b) Preset counter

If a count number is preset, and the count value reaches the set value, COUNT UP is output.
(c) Up/Down counter

This counter can be used for up count and down count also.
(2) Form


| IPSH 16 | $\ldots$ Preset value |
| :--- | :--- |
| APSH \#1500 | $\ldots$ Counter address |
| APSH \#1510 | $\ldots$ Workpiece address |
| LD \#14000 | $\ldots$ CNO |
| STR \#14001 | $\ldots$ UP DOWN |
| STR \#14002 | $\ldots$ RST |
| STR \#14003 | $\ldots$ ACT |
| SUBP 005 | $\ldots$ COUNTER instruction |
| OUT \#11000 | $\ldots$ COUNT UP output |

### 7.7 MACRO INSTRUCTIONS (Cont'd)

(3) Control conditions
(a) Preset value designation (IPSH Xx)

Directly designate a preset value. To designate a variable value, use the PUSH instruction, instead of IPSH, and designate the address. The preset value becomes the address contents.

Example:
PUSH \#1550
If the above designation is given, the two byte of \#1550 and \#155l are used. Do not use \#1551 for others even if only one byte is to be used.
(b) Counter address designation (APSH \#xxxx)

Designate the counter address.
If APSH \#1500 is designated, the continuous two bytes, that is, \#1500 and \#1501, are used for the counter address.
(c) Workpiece address designation (APSH\#xxxx)
Designate an address that is not used by other instructions. 1 byte is needed for one SUBP 005. When two or more SUBP 005 are used, designate an address to each of it.
(d) Initial value designation (CNO)
$\mathrm{CNO}=0$ : The counter cumulative value starts at "0." ( $0,1,2,3,4, \ldots n$ )
CNO = 1: The counter cumulative value starts at "1." $(1,2,3,4,5, \ldots n)$
(e) UP/DOWN designation

UP/DOWN $=0:$ Up counter Initial value is " 0 " with $\mathrm{CNO}=0$ Initial value is "1" with $\mathrm{CNO}=1$
UP/DOWN = 1: Down counter The initial value is the preset value.
(f) Reset (RST)

RST $=0$ : Reset release
RST = 1: Reset
R1 is cleared. The cumulative values is set to the initial value.
(g) COUNT signal (ACT)

$A C T=0$ : The counter does not operate. The Rl contents remain unchanged.
$A C T=1$ : Counts at the rise of "0" to "1."

Note:
If the counter contents are greater than the preset value at the time of power turn on:
In the case of Up counter:
Returns to the initial value with the first ACT.
In the case of Down counter:
Counts down each time ACT is applied, and when the value enters within the preset value, the operation afterward is normal.
(h) COUNT UP output (R1)

Up counter:
Rl is set to "l" upon counting up to the preset value.
Down counter:
When $\mathrm{CON}=0$
R1 is set to "l" when counted down to "0."
When $\mathrm{CON}=1$
R1 is set to "l" when counted down to "1."
(4) Counter use example
(a) Example of using the counter as a preset counter
The number of machined workpieces is counted. When the count reaches the set value, the COUNT UP signal is output.


- Al is the circuit to create Logic "1."
- NC contact of Al is used to clear CNO since the count range used is 0 to 9999 .
- NC contact of Al used to clear UP DOWN as it is used as an UP counter.
- RST, the input signal from the NC unit, is used as the counter reset signal.
- The count signal is the input signal from the NC unit, M02 or M30. NC contact of CUP is contained in this signal the counter does not count once it counted up unless it is reset.
(b) Example of using the counter to memorize the rotating object position.

- Al is circuit to create Logic "l."
- With the rotating object of 10 angles, as shown in the figure, the count start number is 1. Therefore, NO contact of Al is used to CNO to "1."
- REV is a signal that changes according to the rotation direction. It is " 0 " for forward rotation and " 1 " for revers set CNO to "1."
- REV is a signal that changes according to the rotation direction. It is "0" for forward rotation and " 1 " for reverse rotation. Therefore, it operates as an Up counter for forward rotation and as a Down counter for reverse rotation.
- Since no reset signal is used in this example, it is kept to "0" always. Therefore, NC contact of Al is used.
- The CNT count signal is a signal to turn ON/OFF 10 times for one rotation of the rotation object.
- Set 10 and 0 to the preset value addresses of \#1520 and \#1521, respectively.
(2) SUBP 006 (ROTATION)
(1) Function:

This instruction is used to control rotation objects such as blade base, ACT and rotating table. It has the following functions:
(a) Judgement of short-cut rotation direction
(b) Calculation of number of steps between the current position and target position
(c) Calculation of the position of one step before the target position or the number of steps up to one step before the target position.
(2) Form


APSH \#1510 ... Calculation result output address

APSH \#1520 ... Target position input address
APSH \#1530 ... Current position address
IPSH 10 ... Number of rotating object positionings
LD \#14000 ... The position number is from "0" or "1."

### 7.7 MACRO INSTRUCTIONS (Cont'd)

\(\left.\begin{array}{lll}STR \#14001 \& ··· The position data is in <br>

l byte or 2 bytes.\end{array}\right]\)| STR \#14002 | $\ldots$ The rotation direction is |
| :--- | :--- |
| constant or in shortcut. |  |

## (3) Control conditions

(a) Designation of calculation result storage address (APSH\#xxxx)
The ROT instruction calculates the number of steps that the rotating object should rotate, step number of one step before or the position of one step before the target position, and the result is stored in the designated address.
(b) Designation of target position address (APSH\#xxxx)

Designate the address at which the target position is contained. In other words, this is the address in which the $T$ command from the NC unit is contained.
(c) Designation of current position address (APSH\#xxxx)

Designate the address where the current position is stored. For example, this is the address of the counter that memorizes the rotating object position.
(d) Designation of initial value of the position number of rotating object (RNO)

> RNO $=0: \begin{aligned} & \text { The position number of rotating } \\ & \text { object starts from "0." }\end{aligned}$ RNO $=1: \begin{aligned} & \text { The position number of rotating } \\ & \text { object starts from "1." }\end{aligned}$
(e) Designation of number of bytes of position data (BYT)
$B Y T=0$ : Binary 1 byte
BYT = 1: Binary 2 bytes
(f) Designation of whether or not short-cut direction should be determined (DIR)
DIR $=0$ : No determination is made on short-cut direction. The rotation direction is forward only.

DIR $=1:$ Determines short-cut direction.
(g) Designation of operation conditions (POS)

POS $=0$ : Calculate the number of steps to the target position.
POS = 1: Calculates the position or number of steps of one step before the target.
(h) Designation of position or number of steps (INC)

INC $=0$ : Calculates the position number.
INC $=1$ : Calculates the number of steps.
(i) Execution command (ACT)

ACT $=0$ : No execution of ROT instruction. Rl is not affected.
$\mathrm{ACT}=1:$ Execute the ROT instruction. (This is not a rise signal.)
(j) Rotation direction output (RI)

R1 $=0$ : The rotation direction is forward.
Rl = 1: The rotation direction is reverse.
Note:

1. The rotation direction is defined as shown below:


The rotation direction in which the number increases from the indexed position is the forward direction. The direction in which the number decreases is the reverse direction.
2. When the current position is equal to the target position, the calculation result of the number of steps of one step before the target position $(\operatorname{POS}=1$, $\mathrm{INC}=1)$ is "0."
(4) Use of example of ROT instruction

The following shows the control of a 16 -position rotating object, without short-cut control but for deceleration at the position of one step before the target position.

(3) SUBP 007 (CODE CONVERSION)
(1) Function: Converts data using the PC table prepared on the ladder.


- When "3" is instructed for the conversion standard data address with $B Y T=0$, as shown in the above figure, the data of the third address from the head of the table is stored in the conversion data output address. The head address of the table is "0."
- The status when BYT is set to " 1 " is shown below. At this time, check that the size of the conversion data table is in a even byte number.

(2) Form


IPSH 20 ... Size of conversion data table (Number of bytes).
APSH \#1500
... Conversion data address
TPSH \# 9000
... No. of PC table containing conversion data.
APSH \#1510
... Converted data store address.
LD \#14000 ... Data of data table is in 1 byte or 2 bytes.
STR \#14001 ... Reset
STR \#14002 ... Execution
SUBP 007 ... COD instruction
OUT \#14010 ... ERROR output
$\left.\begin{array}{l}-20 \mathrm{H} \\ -30 \mathrm{H} \\ -40 \mathrm{H} \\ \cdot \\ \cdot \\ - \\ -1 \mathrm{AH} \\ -2 \mathrm{BH} \\ -3 \mathrm{CH}\end{array}\right\}$ Conversion data table

### 7.7 MACRO INSTRUCTIONS (Cont'd)

(3) Control conditions
(a) Designation of number of conversion data items (IPSH xx)
Designate the size (number of bytes) of the conversion data table. The maximum size is 256 bytes.
(b) Designation of conversion standard data address (APSH \#xxxx)

Data in the conversion data table is fetched out by designating the number inside the data table. Designate this number inside the table.
(c) Designation of conversion data output address (APSH \#xxxx)
Designate the address to output the data stored in the number inside the table that is designated by Item b. When BYT is "I," data at the higher side is output to the address next to the designated address.
(d) Designation of conversion data table (TPSH xxxx)
Table size is different depending on PC table No.

- 9000-9007: 256 bytes max
- 9008 - 9023: 128 bytes max
- 9024-9087: 64 bytes max
- 9088 - 9215: 32 bytes max
- 9216-9435: 16 bytes max
(e) Designation of data size (BYT)
$B Y T=0$ : When data of the conversion data table is in 1 byte.
BYT = 1: When data of the conversion data table is in 2 bytes.
(f) Reset (RST)

RST $=0$ : No reset.
RST = l: ERROR output Rl is cleared.
(g) Execution command (ACT)
$A C T=0:$ No execution. RI does not change.
ACT = 1: Executes.
(h) Error output (R1)

An error that has occurred during execution of the COD instruction (when a numeric that is greater than the table size). Rl is set to "l" to notify the error.
(4) SUBP 009 (PATTERN CLEAR)
(1) Function: Writes the same numeric for the designated number of bytes from the designated address.


20 BYTES
(2) Form


| IPSH 0 | $\ldots$ Write pattern |
| :--- | :--- |
| IPSH 20 | $\ldots$ |
| Number of bytes to write |  |
| APSH \#1500 | $\ldots$ Head address to write |
| LD \#14000 | $\ldots$ Execution |
| SUBP 009 | $\ldots$ PCLR instruction |
| OUT \#14010 | $\ldots$ Write completion output |

(3) Control conditions
(a) Designation of write pattern (IPSH xx)

Designate a write pattern.
If the pattern is to be variable, use PUSH, instead of IPSH, and designate the address.
(b) Designation of number of bytes to write (IPSH xx)
Designate the number of bytes for pattern clear.
(c) Designation of the head address to write (APSH \#xxxx))
Designate the head address for PATTERN CLEAR start. PATTERN CLEAR is executed for the designated number of bytes from the address.
(d) Execution command (ACT)
$A C T=0: \quad$ No execution.
$\mathrm{ACT}=1$ : Executes.
(e) Write completion output (R1)

Rl $=0$ : Write not completed yet.
Rl = 1 : Write completed.
(5) SUBP 011 (PARITY CHECK)
(1) Function: Parity check (even and odd) of the check data (l-byte data). If not normal, an ERROR output it made.
(2) Form


APSH \#1500 ... Check data address
LD \#14000 $\ldots$ Even/odd parity switch-
STR \#14001 ... Reset
STR \#14002 ... Execution command
SUBP 011 ... PARI instruction
OUT \#14010 ... ERROR output
(3) Control conditions
(a) Designation of check data address (APSH \#xxxx).
Designate the address where the data to be checked is stored. This data to be checked is in 1 byte ( 8 bits).
(b) Odd/Even command (OE)
$O E=0$ : Even parity check
OE = 1: Odd parity check
(c) Reset

RST $=0$ : No reset.
RST $=1:$ Resets ERROR output R1.
(d) Execution command (ACT)
$A C T=0$ : No execution of PARI instruction. R1 does not change.
ACT = 1: Executes PARI instruction.
(e) Error output (RI)

When an odd parity resulting from even parity check or even parity resulting from odd parity check, ERROR output R1 is set to "1."
(6) SUBP 014 (DATA CONVERSION)
(1) Function:

Converts binary data to BCD data, or vice versa.
(2) Form


APSH \#1500 ... Data address to be converted
APSH \#1510 ... Conversion result storing address.
LD \#14000 ... 1-byte or 2-bytes processing.
STR \#14001 ... Conversion from binary to BCD or vice versa.
STR \#14002 ... Reset
STR \#14003 ... Execution
SUBP 014 ... DCNV instruction
OUT \#14010 ... ERROR output
(3) Control conditions
(a) Input address of data to be converted (APSH \#xxxx)
Designate the address where the data to be converted is stored. In the case of BYT $=1$, two continuous bytes are used for the address.
(b) Conversion result storing address

This address stores the converted data. Where $\mathrm{BYT}=1$, continuous bytes are used.
(c) Designation of number of bytes of data (BYT)
$B Y T=0:$ The processing data is in one byte.
BYT $=1$ : The processing data is in two bytes.
(d) Designation of conversion form (CNB)

CNV $=0$ : Converts binary data to BCD data.
$C N V=1$ : Converts $B C D$ data to binary data.
(e) Reset (RST)

RST $=0$ : No reset.
RST = l: Resets error output Rl.

### 7.7 MACRO INSTRUCTIONS (Cont'd)

(f) Execution command (ACT)
$A C T=0 ; ~ N o ~ e x e c u t i o n . ~$
$A C T=1:$ Execution.
(g) ERROR output (R1)

R1 = 0: Normal
$\mathrm{Rl}=1$ : Abnormal (The data to be converted is binary data when CNV $=1$, or the byte length was exceeded when $\mathrm{CNV}=0$. When $\mathrm{BYT}=1 ; \mathrm{CNV}=0, \mathrm{Rl}$ is not output unless BCD data is more than $2711(\mathrm{H})$.
(7) SUBP 017 (DATA SEARCH)
(1) Function:

Searches the same data as the input data in the table. If there is, the relative address from the table head is stored in the output data address. If the same data is not found, an ERROR output is made.


Note: Check that the table size is in as even byte number when $\mathrm{BYT}=1$.
(2) Form


IPSH 20 ... Number of bytes of data table
APSH \#1500 ... Head address of data table
APSH \#1510 ... Search data address
APSH \#1520 ... Table inside number storing address

| LD | $\# 14000$ | $\ldots$ |
| :--- | :--- | :--- |
|  | The processing data is in |  |
| one byte or two bytes. |  |  |

(3) Control conditions
(a) Designation of number of data items of data table (IPSH xx )
Designate the data table size (number of bytes).
(b) Designation of head address of data table (APSH \#xxxx)

Designate the head address of the data table. The data table may be created in any place.
(c) Designation of input data address (APSH \#xxxx)
Designate the address where the data to be searched is stored.
(d) Designation of output data address (APSH \#xxxx)
If the searched data is found $(\mathrm{Rl}=0)$, the number inside the table where the data is stored is output. Designate the output address.
(e) Designation of data size (BYT)
$B Y T=0$ : The stored in the data table is in one byte.
BYT $=1:$ The data stored in the data table is in two bytes.
(f) Execution command (ACT)
$A C T=0: \quad$ No execution
$\mathrm{ACT}=1:$ Execution
(g) Reset (RST)

RST $=0$ : Not reset.
RST = 1: Reset. Rl is cleared.
(h) ERROR output (Rl)

Rl = 0: The search data is found.
Rl = l: The search data is not found.
(8) SUBP 018 (INDEX DATA MOVE)
(1) Function: Reads or re-writes data from the data table.
(a) Read

- "3" was designated as the table inside number and the contents were read.

(b) Re-write
- "3" was designated as the table inside number and the contents were re-written.

(2) Form


IPSH $20 \quad \ldots \begin{aligned} & \text { Number of bytes of data } \\ & \text { table }\end{aligned}$
APSH \#1500 ... Data table head address
APSH \#1510 ... I/O data storing address
APSH \#1520
... Table inside number storing address

| LD $\# 14000$ | $\ldots$ The processing data is in |
| :--- | :--- | :--- |
| one byte or two bytes. |  |

(3) Control conditions
(a) Designation of number of data items of data table (IPSH xx)
Designate the data table size (number of bytes).
(b) Designation of data table head address (APSH \#xxxx)
Designate the data table head address.
The data table may be created in any place.
(c) Designation of I/O data storing address (APSH \#xxxx)
RW $=0$ : Address to store output data.
RW = 1: Address to store input data.
(d) Designation of table inside number storing address (APSH \#xxxx)
Designate which data in the data table should be read or re-written with a table inside number. The table inside number designates the storing address.
(e) Designation of data size (BYT)
$B Y T=0:$ The data stored in the data table is in one byte.
BYT = 1: The data stored in the data table is in two bytes.
(f) Designation of read or re-write (RW)

RW $=0$ : Reads data from the data table. $R W=1:$ Re-writes data from the data table.
(g) Reset (RST)

RST $=0$ : Not reset.
RST = 1: Reset. Rl is cleared.
(h) Execution command (ACT)
$\mathrm{ACT}=0$ : No execution
$\mathrm{ACT}=1:$ Execution
(9) SUBP 023 (MESSAGE DISPLAY)
(1) Function: Displays messages on the CRT of NC.


### 7.7 MACRO INSTRUCTIONS (Cont'd)

The message is displayed under the title of USERS MESSAGE.

Max. number of characters and types of messages are as follows. One of each is selected.

| Max. number of <br> characters | Type | Table address |
| :---: | :---: | :---: |
| 32 bytes | 128 | $\# 9088$ to \#9215 |
| 64 bytes | 64 | $\# 9024$ to \#9087 |

The following shows the max. number that can be displayed on the CRT at the same time.

| Max. number of <br> characters | Number of simultaneous displays |
| :---: | :---: |
| 32 bytes | 2 sets |
| 64 bytes | 1 set |

- Up to 4 messages are displayed on the CRT screen. If there is a request to display more messages, low order bits are given the priority. Messages of higher priority are displayed sequentially.
- The displayed messages set the corresponding bits to "l," and messages to be cleared clear the corresponding bits. The figure below shows the correspondence.

Display request

Display status

Display request

Display status


Note:

1. Do not set bits containing no message data to "1."
2. This instruction is an instruction to display messages on the CRT screen. The instruction cannot set NC to an alarm state (1-block atop, decelerated stop, and immediate stop).
(2) Form


| Table addresses | Display request | Message contents |
| :---: | :---: | :---: |
| \#9216 | 115000 | SPiNDLE-ALARM |
| \#9217 | \$15001 | MO6 ERROR |
| 19218 | 115002 | TAPPING ERROR |
| \#9219 | \#15003 |  |
| $\ldots$ | $\underline{3}$ | $\longrightarrow$ |
|  |  |  |
| 19229 | \$15015 | UNUSABLE.S-CODE |
| *9230 | \#15016 | UNUSABLE M-CODE |
| \%9231 | 15017 | PARAMETER ERROR |

APSH \#1500 ... Message data control address
IPSH 1 ... Size of message control address
IPSH 32 ... Number of characters of one message data
TPSH 9216 ... Top of PC table containing message.
SUBP 023 ... DISP instruction
(3) Control conditions
(a) Designation of message control address (APSH \#xxxx)
Designate the head address that request the message.
(b) Designation of size of message control address (IPSH xx)
Designate the size (number of bytes) of message control address.
For example, when the message control address is designated as APSH \#1500 if IPSH 1 is specified, continuous 4 bytes from \#1500 are used, and if IPSH 2 is specified, continuous 8 bytes from \#1500 are used.
Note: Up to 16 types of messages are available when IPSH 1 is specified.
(c) Designation of number of characters per message (IPSH xx)
The number of characters for each message varies. Designate the maximum number of characters in the PC table to be used.
(d) Designation of top number of PC table containing message (TPSH xxxx)
(4) DISP instruction use example

When contacts AL1 - AL4 are set on, the message corresponding to the request bits are displayed on the CRT screen, and deceleration stop is performed. The display goes out when a reset signal is given.

(5) Improving USERS MESSAGE function (J50M only)

This fucction displays messages on NC CRT screen from PC input signals having operation mistakes or machine defects.

The following messages are displayed:
(i) Regarding ERROR code and ERROR contents.
(ii) Showing machine operation condition.
(iii) Showing operation procedure, etc.

These messages can be displayed in NC USERS MESSAGE screen.

There is no distinction between the ways of displaying messages for easy operation.

USERS MESSAGE display selection

USERS MESSAGE display is selected by the following operation:
(1) The established USERS MESSAGE 1 display is selected by depressing ALM key to select alarm display.
(2) Added USERS MESSAGE 2 display is selected by depressing ALM key again.
(3) Depressing the ALM key again calls up USERS MESSAGE 1.

MESSAGE 1.
a. MESSAGE DISPLAY instruction

Two SUBP023s can be used on the ladder.
First SUBP023


Second SUBP023


SUBP023 which has been used first on the ladder is displayed under the title of USERS MESSAGE 1 on the message screen (USERS MESSAGE 1).

Depress ALM key, and SUBP023, which has been used later, is displayed under the title of USERS MESSAGE 2 on the message screen (USERS MESSAGE 2).

### 7.7 MACRO INSTRUCTIONS (Cont'd)

By depressing ALM key again, the display is reverse displayed to USERS MESSAGE 1 from USERS MESSAGE 2.

Note: USERS MESSAGE 1 has only on display. By depressing PAGE key the previous display is called up.
b. Display specifications

Number of characters in a message and message types.

| 16 characters | $* 220$ types (Max.) |
| :--- | :--- |
| 32 characters | $* 128$ types (Max.) |
| 64 characters | $* 64$ types (Max.) |

For two SUBP023s, the same characters can be used. In this case, however, the total number of the message types of two SUBP023s should be less than the maximum of each message.

Display table
64 character
64 addresses between 9024 and 9087.
32 character
128 addresses between 9088 and 9215.
16 character
220 addresses between 9216 and 9435 .
USERS MESSAGE 2 display range
Simultaneous display range
Valid width ............. 30 characters
Valid lines ............. 10 lines
Valid No. of message ... 3 to 5 types within the range of valid 10 lines or less.



Note: When the table shown above is used for another SUBP023, range of display table is decreased.

When the display table is used for another SUBPO2 for other purposes, max. display type is limited available table capacity.

When making a table, put "SPACE" if necessary.
Characters under "FF" are disregarded.

## 8. SEQUENCE PROGRAM EXAMPLE

### 8.1 SERIES CONNECTION


(2)

(3)

(LIST)

| LD-NOT | \#14210 | AND-NOT \#14910 |  |
| :--- | :--- | :--- | ---: |
| AND | \#10120 | OUT | \#13174 |

### 8.2 PARALLEL CONNECTION

(1)

(LIST)
LD \#10000
OR \#10063
OUT \#13000
(2)

(LIST)

| LD | \#15000 | OUT | \#11067 |
| :--- | :--- | :--- | :--- |
| OR | \#15001 | OUT | \#13187 |
| OR | $\# 15002$ |  |  |

(3)
(a)

(LIST)

| LD | $\# 12006$ | AND-NOT \#14661 |  |
| :--- | :--- | :--- | :--- |
| OR | $\# 12007$ | OUT | $\# 14301$ |

## OUT \#13164

(b)


Note: In this program, coding cannot be made. Make a sequence as described in (3) a, or change the ladder as follows.


### 8.3 SERIES AND PARALLEL CONNECTION

(1)
(a)

(LIST)

| LD | $\# 13176$ | AND | $\# 14431$ |
| :--- | :--- | :--- | :--- |
| OR-NOT | $\# 12191$ | OUT | $\# 14050$ |

### 8.3 SERIES AND PARALLEL CONNECTION

(Cont'd)
(b)

(LIST)

(LIST)

| LD-NOT | $\# 14200$ | AND-NOT \#12181 |  |
| :--- | :--- | :--- | :--- |
| AND | $\# 14111$ | OUT | $\# 15100$ |
| OR | $\# 15100$ |  |  |

(b)

(3)

(LIST)

| LD | $\# 12190$ |
| :--- | ---: |
| OR | $\# 12192$ |
| STR-NOT | $\# 12190$ |
| OR | $\# 14114$ |

ATR-NOT \#12192
OR \#14361
AND-STR
OUT \#13166
AND-STR
(4)

(LIST)

| LD | $\# 14003$ | AND | $\# 14220$ |
| :--- | :--- | :--- | :--- |
| STR | $\# 14001$ | AND | $\# 10000$ |
| AND | $\# 13020$ | OR-STR |  |
| OR-STR |  | OR | $\# 14005$ |
| AND-NOT \#13021 | AND | $\# 10040$ |  |
| AND | $\# 10001$ | OUT | $\# 13020$ |

STR \#14003

### 8.4 MASTER CONTROL RELAY APPLICATIONS

(1)

(LIST)

| LD | \#10007 | LD | \#10011 |
| :--- | ---: | :--- | :--- |
| AND | $\# 10000$ | OUT | \#14011 |
| AND-NOT \#14023 | LD | \#10012 |  |
| MCR |  | OUT | $\# 14012$ |
| LD | $\# 10010$ | END |  |
| OUT | $\# 14010$ |  |  |

The above ladder has the same meaning as that of the ladder below.


(LIST)

| LD | $\# 12190$ | OUT | \#14101 ; M04 |  |
| :--- | :--- | ---: | :--- | :--- |
| MCR |  | DEC | \#1222, 05H |  |
| DEC | $\# 1222,03 H$ | AND | \#12003 |  |
| OUT | $\# 14100 \quad ;$ M03 OUT | \#14102 | M05 |  |
| DEC | $\# 1222,04 H$ | END |  |  |

This is the code detection ladder for $M$ code. By use of MCR, ladder can be completed without inserting $M F$ in each $M$ code.

## 9. SEQUENCE PROGRAM ONLINE EDITING SYSTEM

This section describes the functions provided by a "sequence program editor (JDUO1)" in temporary connection with the NC unit YASNAC J50L or J50M, together with the operating procedures for the editor.

The functions of the sequence program editing system fall into three major categories:
(1) Editing Sequence Programs

To erase, alter and insert commands from, in and to sequence program.
(2) Providing Hard-copy of Edited Sequences Programs
To punch a sequence program onto a tape and transfer data to $\mathrm{P}-\mathrm{ROM}$ writer.
(3) Checking Edited Sequence Programs

To check a sequence program in C-MOS and another program written in P-ROM through execution.

The following paragraph discuss the functions and operating procedures in detail.

### 9.1 BLOCK DIAGRAM OF SEQUENCE PROGRAM EDIT SYSTEM

Figure below shows the hardware constitution of sequence program edit system.


Fig. 9.1 Block Diagram of Sequence Program Edit System

### 9.1 BLOCK DIAGRAM OF SEQUENCE PROGRAM EDIT SYSTEM (Cont'd)

(1) The sequence program editor (JDUO1) should be mounted on the CPU rack in the NC unit before being wired.
(2) To operate a sequence program editing system, use the NC operator panel with a CRT as an operator panel for the editing system.
(3) A tape reader is used to load into sequence program editor memory a list tape with a sequence ladder coded in it or a P-ROM format tape written in machine language.
(4) A tape puncher is used to punch out the final sequence program that was edited and checked on a list tape or P-ROM format tape.
(5) A commercially available P -ROM writer can be connected to the NC RS232C interface to write the final sequence program into $P-R O M$.

## 9. 2 SEQUENCE PROGRAM EDITOR (JDU01)

(1) The name and the type of the sequence program editor are as follows:
Name: Sequence Program Editor
Type: JZNC-JDU01
External view of the JDUO1 is shown in Fig. 9.2.
(2) The JDU01 has a C-MOS memory backed up by battery. It can store up to a 128 k -byte sequence program to be edited. The stored sequence program is on the level of the P-ROM format in machine language.
(3) JDU01 components along with their functions are listed below.
(a) Two mounting holes with hooks :

Mounts the JDUO1 with attached hook on the CPU rack in the NC unit.
(b) CNAI ( 120 core) and CNF ( 80 core) connector :
: Supplies power ( +5 V ) to the JDU01
: Used to connect the NC main section with the PC section.
(c) ROM/RAM select switch :

Selects P-ROM in the P.C. system or C-MOS memory in the JDU01 for operation or controlling.

## 9. 3 CONNECTING SEQUENCE PROGRAM EDITOR

Follow the steps given below to connect the JDU01.
(1) When the JDUO1 is mounted on the NC CPU rack, NC CPU unit power should be turned OFF.
(2) Mount the ROM (No. 40) on the JSD board from the PC50 board.
(3) Replace the PC50 board with the JSD board.


JZNC-JDU01


Fig. 9.2 CPU Rack

### 9.4 EDIT SYSTEM OPERATOR'S STATION

The NC operator's station with CRT is used for sequence program editing, when used as a sequence program editing unit.

Fig. 9.3 shows the NC operator's station respectively for YASNAC J50L and J50M.


Fig. 9.3 Operator's Station for J50L/J50M
(1) POWER ON/OFF Pushbuttons

- POWER ON pushbutton

To turn on the power for the control: Depress the pushbutton first to turn on the control power and depress it again to turn on the servo power. (Push this button to recover the servo power after an emergency stop.)

- POWER OFF pushbutton

To turn off the power for the control: Depress it to turn off both the servo and control powers.
(2) DATA Key

For 0 to 9, data keys of 0 to 9 are used. For hexadecimal A to $F$, address keys of $A$ to $F$ are used. Commands and address input can be made by using address keys.
(3) CAN (cancellation) key:

For cancellation of the input data.
(4) WR (write) key:

For storing the input data into buffer storage.
(5) CURSOR Keys

The CURSOR control key is used to move the cursor. It is used to start address search.

- Depressing anson key moves the cursor
forward.
- Depressing $\circlearrowleft$ key moves the cursor backward.
- Keeping the cursor control key depressed makes the cursor move automatically forward or backward.
(6) PAGE Keys

Depressing the paGE key increases the editing page by one. Depressing the $\underbrace{\infty}_{\text {PAGE }}$ key moves the cursor backward.
(7) NEXT Key (Function Mode Select Keys)

Depressing the NEXT key increases the function mode number by one. Made 6 changes to mode $l$ by depressing the NEXT key. For details of mode 1 to 6 , refer to par. 9.5 .
(8) ERS , INS, ALT, and EOB Keys
(a) ERS key:

For erasure of a block of data in a sequence program.
(b) INS key:

For insertion of a block of data in a sequence program
(c) ALT key:

For alteration of a block of data in a sequence program

### 9.4 EDIT SYSTEM OPERATOR'S STATION (Cont'd)

(d) EOB key:

For storing a block of data in a sequence ladder. The block stored using the EOB key will be the last block in a sequence program.
(9) IN, VER , and OUT Keys
(a) IN key:

To start storing data on paper tape into memory through tape reader.
(b) VER key:

To start verifying between memory data and punched tape data.
(c) OUT key:

To start outputting various data in memory through data $I / O$ interface.
(d) RESET key:

To return the editing pointer to the head of sequence ladder. Also used for releasing alarm codes if their causes are eliminated.

### 9.5 FUNCTION MODE OF EDIT SYSTEM

When the control unit is used as a sequence program unit, four function modes can be selected. Use the NEXT key for mode selection.

J50L/J50M PC System Structure

(1) JDUO1 board ROM/RAM select switch

$(2) \rightarrow$ : Stores the edited D-RAM data in C-MOS of JDUO1 board. (See (4) in the column of MODE 4.)

Table 9.1 List of Function Modes and Functions

| Function <br> Mode No. | Function Mode | Function |
| :---: | :---: | :---: |
| Mode 1 | Edit mode (LADDER EDIT) | - Alteration, insertion, and deleting sequence programs, address search, and writing by MDI. <br> -Storing, collating, and punching out of P-ROM former tape. |
| Mode 2 | List tape mode (SOURCE TAPE) | -Storing, collating, and punching out of list tape. |
| Mode 3 | PROM writer mode (ROM WRITER) | - Transferring sequence programs to P-ROM writer. |
| Mode 4 | Parameter mode (PARAMETER) | (1) Registration of version number <br> (2) Registration of tape comments <br> (3) Setting Baud rate <br> (4) Transfer of DRAM to C-MOS <br> (5) Transfer of P-ROM to DRAM or C-MOS to DRAM. <br> (6) P-ROM type selection <br> (7) Resetting of edit area <br> (8) Returning to NC mode <br> (9) I/O device selection |
| Mode 5 | PC data edit mode (PC TABLE EDIT) | (1) Editting of PC table and address searching <br> (2) Storing, collating, and punching-out of P-ROM format tape |
| Mode 6 | Address check mode (ADDRESS CHECK) | Checking for address duplication in sequence program. |

### 9.6 HOW TO ENTER EDITING SYSTEM MODE

Given below are the EXIT STEPS to leave the NC system mode (NC Mode), and to enter the editing system mode (SD mode) in which the device is used as sequence program editing system. After switchover to the SD mode, the device permits operations described in par. 9.7 through 9.11.

### 9.6.1 When NC Unit is in Offline State

 (System NO. $6 \rightarrow$ SD MODE)The NC unit in the offline state is an NC unit that cannot operate in the NC mode upon power-on, with no sequence program stored in PC P-ROM or JDUO1 C-MOS.

Switching from the offline state to the SD mode requires the following operations, provided that the JDU01 has been connected as explained in par. 9.3 :
(1) Set the System No, switch to 6 .
(2) Snap the ROM/CMOS select switch to RAM on the JDUO1.
(3) Depress the POWER ON pushbutton to apply power. A comment "OPTIONAL JOB" will appear on the CRT.


Fig. 9.4
(4) Deress the $X, S$ and $D$ keys, in that order. Then depress the ORG key. A comment "SEQUENCER EDITOR" will appear on the CRT.

```
*SEQUENCER EDITOR*
```

Fig. 9.5

About 2 seconds later, MODE 1 of the $S D$ mode is entered.


Fig. 9.6
(5) Then operate the PAGE keys to select one of six MODEs in the SD mode.

[^2]
### 9.6.2 When NC Unit is in Online State (System NO. $4 \rightarrow$ SD MODE)

The NC unit in the online state is an NC unit that can operate in the NC mode upon power-on, with the sequence program stored in P-ROM or C-MOS.

Switching from this online state to the SD mode requires the following operations, provided that the JDU01 has been connected as explained in par. 9.3:
(1) When the sequence program is stored in P-ROM, snap the ROM/RAM select switch to ROM on the JDU01. Set the switch to C-MOS for the program stored in C-MOS.
(2) Depress the POWER ON pushbutton to apply power (set the System No. switch to 0 or 4 beforehand). The NC mode will be entered.
(3) When a test run is performed here for sequence program check, stop all NC functions by Feed Hold or other operations and press the RESET key afterward.
(4) Set the System No. switch to (4.
(5) Depress the DGN function key, and depress the NEXT key. A comment "(STORED)" will appear following another comment "DIAGNOSIS" on the CRT.
(6) Depress the $X, S$ and $D$ keys, in that order. Then depress the ORG key. A comment "SEQUENCER EDITOR" will appear on the CRT (Fig. 9.5). About 2 seconds later, MODE 1 of the SD mode is entered (Fig. 9.6).
(7) Then operate the PAGE keys to select one of six MODEs in the $S D$ mode.

## NOTE

1. The NC unit in the online state can enter the SD mode by the following parameters. \#6030Dl = 1 for J50M. \#6030D7 = 1 for J50L.
2. After switchcover from the online state to the SD mode, the PC output signals remain as they were just before the SD mode was entered.
Example:
A flashing PC output signal remains on when SD mode is selected during on state.
3. The minimum condition for the $S D$ mode to be entered by the above steps is that "RTH" (end command of highspeed sequence program) and "RET" (end command of sequence program) have been written in $\mathrm{P}-\mathrm{ROM}$ or C-MOS.

### 9.7 EDITING MODE (MODE 1)

This mode permits the following operations:
(1) After, insert, erase, and address search operation on sequence programs.
(2) MDI write operation on sequence programs.
(3) Loading, verifying and punching out P-ROM format tapes.

### 9.7.1 Sequence Program Editing

(1) CRT display in MODE 1
(a) As shown below, 10 lines of a sequence program stored in C-MOS are displayed in MODE 1. A blank line is counted as one line.


Fig. 9.7
(b) A line number is a serial number attached to a closed circuit group beginning with a contact input command and ending with a contact output command.

(c) A cursor is positioned to the command to be edited. See the next paragraph "Address search function" for how to specify the cursor.


Fig. 9.8

Note: If MODE 1 of the $S D$ mode is entered from the System No. switch at 6, an error comment "*DISASSEMBLE*" will appear on the CRT because no sequence program is currently stored. In this case, enter the parameter mode of MODE 4 and clear the edit are ((6) in par. 9.10) to reset the error comment. Commands "RTH" and "RET" will appear on the CRT. Then normal edit operations are possible.

## (2) ADDRESS SEARCH

Address Search searches the commands or line to be edited. The searching procedure is as follows.
(a) Key in the commands to be searched

Keying in "0," "R," "WR," "1," "0," "0," "0," "0," through the keyboard causes OR \#10000; to display at the bottom of the CRT screen.
(b) Depress the

key.
Search starts. When the search is completed, ten-line commands including the searched command will be displayed on the CRT screen.
(c) If the keyed-in command cannot be found, "*ERR008*" will be shown on the CRT screen. Release the alarm code by depressing CAN or RESET key.


CURSOR indicates the searched command.
Fig. 9.9

Note:

1. The command can be searched by keyingin the part of the command data.
Example: For DST \#1200, \#1100, FF commands keying-in "D." "S," "T," "WR" can search the DST commands regardless of \#1200, \#1100, and FF.
2. Address search can be done by using only one address
Example: For DST \#1200, \#1100, FF commands, keying-in "\#" "1," "2," "0," "0," "WR" can search the commands which use \#1200 regardless of DST, \#1100, and FF.
3. Address search can be done continuously. Searching can be continued if
key is pressed again after address search. Depress CAN key to quit searching.
4. When the data to be searched is near the CURSOR , use the CURSOR key to reach the required data.

## (3) Key input operations

Below are the steps to key in commands and display them at bottom left on the CRT screen for editing or address search.
(a) Press the ADDRESS keys to sequentially key in the alphabetics of the commands to be entered.

## Example:



Alphabetic strings will appear at bottom left of the CRT screen.
(b) Depress the WR key.
i. For commands not requiring address numbers (SET, END, etc.), a semicolon (;) is displayed after each to complete the key-in operation.
ii. For commands requiring address numbers (OR, MOV, etc.), a symbol "非" is displayed after each to prompt further entry.
iii. Entering an alphabetic string other than the commands causes a comment "*ERR01*" to appear on the CRT. This is reset by depressing the CAN or RESET key.
(c) Key-in address numbers (followed by bit numbers if necessary). For commands requiring one address number (e.g., OR), entering the required number of digits causes a semicolon (;) to appear automatically after each number, thus completing the key-in operation.
(d) Press the WR key. For commands requiring two address numbers (e.g., MOV), symbols ",\#" will automatically appear after entry of the first number.
(e) Key in the next address number, and the number will be displayed.
(f) Press the WR key. A semicolon (;) will be displayed to complete the key-in operation. If an inadvertent key is pressed in each section explained above, press the CAN key and then press the correct key.


Fig. 9.10


Fig. 9.11


Fig. 9.12
The above procedure covers most of the commands, with only a few differences for some. In any case, a semicolon (;) appearing at the end of the entered data indicates the end of the key-in operation. On the data thus keyed in, address search and editing functions by the INSRT, ALTER and ERASE keys are available.
(4) Edit Operation ( ALTER, INSRT, ERASE)

The command specified by the cursor can be altered, inserted or erased.

## (a) Alter operation

Depress the ALTER key. The command specified by the cursor will be erased and replaced by the command just entered. After alteration, the command that replaced the old one remains specified.

### 9.7.1 Sequence Program Editing (Cont'd)



Fig. 9.13
(b) Insert operation

Press the INSRT key. The command just entered will be inserted following the command specified by the cursor. After insertion, the command just inserted remains specified.


Fig. 9.14
(c) Erase operation

Press the ERASE key. The command specified by the cursor will be erased. After erasure, the command following the erased command is specified.


Fig. 9.15
(5) Low-speed processing sequence program division

When the edit operation of sequence program is completed in the edit mode, the sequence program should be divised for low speed processing.
Depress the RESET key, and then ORG key with MODE 1. The programs are automatically divided for low-speed processing and number of section count is indicated.

### 9.7.2 MDI Write Operation on Sequence Program

In MODE 1 , a sequence program can be written by MDI key-in operations from the beginning. The write operations are as follows:
(1) Operate the NEXT to select MODE 4.

Clear the edit area.
For the details, refer to par. 9.10 (7).
(2) Operate the NEXT key to return to MODE 1. This operation returns the cursor to the beginning of memory. Commands "RHT and "RET;" will appear on the CRT.
(3) Key in the desired command by the operation of par. 9.7.1 (3) on page 51.
(4) Depress the INSRT key, and the command just keyed in will be inserted following the command specified by the cursor. The inserted command will be specified anew.
(5) Repeat the operations of (3) and (4) above to write the sequence program consecutively.
(6) Finally, depress the $R, E, C T$ and $E O B$ keys, in that order, to complete the writing of the sequence program (RET = sequence program end command).

## NOTE

1. Depressing the EOB key inserts the command just keyed-in following the command specified by the cursor, and erases all the subsequent commands. That is, the command stored by the EOB key becomes the last command of the sequence program at that time.
2. Consequently, in the edit operation of par. 9.7.1 (4), the EOB key can be used to erase all commands following a specific command (see Fig. 9.16].

Depressing the EOB key inserts AND-NOT command after OR command and deletes all the commands stored after AND-NOT.


Fig. 9.16
3. Section count display function: Upon completion of a ladder sequence editing process, depress the RESET or ORG key to produce the section and CHECK SUM (total). Then the section count is displayed as shown below. CAN or RESET key can clear this.


Fig. 9.17
4. Search function of section marked ****

After finding the section count by keying ORG, the portions in the ladder where the section is inserted can be searched.
(a) Key-in *0 and then, SHIFT four times. The section count " n " (two digits) to be searched, and WR.
(b) Key-in
(c) When the search process has been completed, the sequence ladder for that portion is displayed. If *ERR.008* (search error) is displayed, clear it by depressing the RESET key.

### 9.7.3 P-ROM Format Tape Input/Output Function (IN, OUT)

MODE 1 permits a P -ROM format tape on the machine language level to be inputted, verified and punched out.
(1) Inputting P-ROM Format Tape ( $\mathbb{N}$ )

A sequence program stored in the form of P -ROM format tape is reedited.
(a) Set a P-ROM format tape on the tape reader.
(b) Depress the $\mathbb{N}$ key. This will move the contents of the P-ROM format tape into PC50 RAM memory (edit area). If an inadvertent tape read operation or an erroneous entry is detected, *ERR003* is displayed on the CRT screen and the tape stops on an 16 K -byte boundary. Although depressing the $\operatorname{IN}$ key again can reset the error and continue loading the tape contents, it is recommended to run the tape from the beginning. Should the error recur, the tape is not usable.
9.7.3 P-PROM Format Tape Input/Output Function (IN , OUT) (Cont'd)
(2) Punching Out P-ROM Format Tape (OUT

An edited sequence program is punched out onto a P-ROM format tape.
(a) Connect the tape puncher (see NOTE 1) via the data I/O interface option of the NC unit.
(b) Depress the RESET key and ORG key orderly. The cursor will return to the beginning of the sequence program.
(c) Depress the OUT key. The contents of PC50

RAM memory will be punched out onto a P-ROM format tape on the machine language level.

REMARKS:
i. To verify whether or not the contents are punched out correctly, continue the vertification of (2) above.
ii. A feed hole punch portion about 75 cm long is provided at the both ends of the tape.

## NOTE

1. The storage devices and tape punchers for P-ROM format tapes and list tapes are designated by MODE4, FUNCTION 10.
2. Storing data on P-ROM format tape is only about one tenth as bulky as that on list tapes. However, a list tape cannot be produced directly from a P-ROM format tape. This format is convenient for punching each substantial amount of data for storage.

### 9.8 LIST TAPE INPUT/OUTPUT MODE (MODE 2)

MODE 2 allows a list tape with a sequence ladder coded in PC instruction words to be loaded, verified and punched out.
(1) CRT Display in MODE 2

Operate the PAGE keys to select MODE 2, and the following screen will appear on the CRT:


Fig. 9.18
Note: SOURCE TAPE should be regarded as the same as LIST TAPE.
(2) List Tape Definition and Rules on List Tape Creation
(a) The list tape is defined as a punched tape with a sequence ladder coded in PC instruction words. See Fig. 9.19.

(b) The rules for creating a list tape are as follows:
i. The list tape may be punched either in EIA or ISO code; the code is automatically identified when the tape is read in.
ii. The beginning and end of the list tape should be in the following format:

For EIA code


For ISO code

iii. The following rules should be observed in punching a list tape from a handwritten list (Fig. 9.20):
(1) Punching CR (or LF/NL) at the beginning of a line specifies a line feed.
(2) All blanks must be filled with space code.
(3) In a label part, punch a number (line No.) or space.
(4) For PC table, follow the format in Fig. 9.21.

## NOTE

Line numbers and comments are only for readability and are insignificant in assembling. The line numbers may or may not match those that were entered; The editor internally processes the line numbers regardless of the entered line numbers for display on the CRT and printing. No comments are stored in memory, nor are they displayed on the CRT or printed out. "\#" is used for ISO code.
" N " is used for EIA code.


Fig. 9.20


Fig. 9.21
(3) Assembling and Storing List Tape ( $\mathbb{N}$ )

A designed sequence ladder is coded and its data used for editing.
(a) Set a list tape on the tape reader.
(b) Depress the $\mathbb{N}$ key. List tape data will be loaded into DRAM memory (edit area) as they are assembled. If a code error or punch error is detected, the tape is kept read in and the error is loaded as "NOP" code. No error indication is given.

> Note: "Assemble" operation means converting PC instruction words in list form into machine language. It follows that the PC50 edit area holds data in machine language.
(4) Punching Out List Tape (OUT)

The edited sequence program for listing on a printer is punched out in the form of list tape.
(a) Connect the RS232C or equivalent tape puncher via the data I/O interface option of the NC unit. Refer to MODE 4 FUNCTION 10.
(b) Depress the RESET key. The cursor will return to the beginning of the sequence program.
(c) Depress the OUT key. The contents of PC50 memory will be punched out onto a list tape of the PC instruction word level.
(5) Reading-in, punching-out, and verifing of PC data tables (IN, OUT, VER )

Operations of reading-in, punching-out, and verifing PC data tables should follow the procedures shown below.

Reading-in (IN )... Press $T$ and $I N$ keys. Punching-out ( OUT ) ... Press $T$ and $O U T$ keys.
(6) PAUSE function

Since length of list tapes tends to become long, more than two tapes are sometimes needed. Therefore, PAUSE function is provided for the $\mathbb{I N}$, and $O U T$ operations of list tapes.
(a) OUT (punch-out)

If CAN key is pressed while a list tape is punched out, then up to the end part (i.e. AND \#10013; \%) of a command code will be punched out, "OUT PAUSE" will be displayed on the CRT, and the punching out stops. If the OUT key is pressed again in this state, then following data will be punched out. However, if RESET key is pressed then the punching out starts again from the beginning of the data.
(b) IN (reading in and verifing)

For reading-in and verifing operations of a list tape, when the last "号" of a command code is read-in, "IN PAUSE" is displayed and a corresponding operation stops. If IN key is pressed after changing a
tape then following data will be stored or verified. However, if RESET key is pressed, then storing or verifing starts again from the first part of the data.

## NOTE

1. Continue the verification of (2) above to check that the program is correctly punched out.
2. A feed hole punch portion about 75 cm long is provided at the beginning and the end of the punched-out tape.
3. The above steps apply to the punching of data in ISO code. To punch out in EIA code, press the OUT key while keeping the $E$ key depressed.

### 9.9 P-ROM WRITER MODE (MODE 3)

This mode is used to transfer a sequence program or PC table data from DRAM memory to a commercially available P-ROM writer connected to the control via the RS232C interface of the NC.

## (1) CRT Display in MODE 3

Operate the NEXT key to select MODE 3. The following screen will appear:
(a) Ladder data


The line " 30 " indicates the 64 k bytes edit area of the JDU01, and the location number shows the field in which the sequence program is actually written. Numbers 30 represents location numbers of P-ROMs for further identification. That is, the edit area is represented in terms of P-ROMs.

To transfer PC table data, set the display shown below by
(b) PC table data


Fig. 9.24
(2) Selection of P-ROM Writer
(a) The user is expected to prepare a commercially available P -ROM writer with the following 4 features:
(i) Reading in the "Intel Hex Format" is available for data transfer.
(ii) Writing to the P-ROM 27 Cl 1024 (made by HITACHI Ltd.) is available.
(iii) The RS232C interface is provided.
(iv) One of the data transfer baud rates shown in Table 9.3 on page 61 is usable.
(b) The following are some recommended $\mathrm{P}-\mathrm{ROM}$ writers that meet the above requirements:

Table 9.2 Recommended P-ROM Writers

| P-ROM Writer | Manufacturer |
| :---: | :---: |
| EPROM Programmer : R4945 | ADOBANTESUTO INC. |

(3) Writing Operation to P-ROMs

Steps to write to P-ROMs by use of the P-ROM writer R4945 of ADOBANTESUTO INC. For details, refer to the instructions for P-ROM writers :

### 9.9 P-ROM WRITER MODE (MODE 3) (Cont'd)

(a) Transfer conditions of R4945
(i) Selection of device

Select "HN27C1024" made by HITACHI.
[Manufacturer's setting]

- Key-in TYPE, 1 and SET.
- Select "Hitachi" by using $\boldsymbol{\Delta}$ or $\nabla$ key.
- Depress SET key.
[Setting of device type]
- Key-in TYPE, $D$ and SET.
- Select "HN27C1024/H" by using $\boldsymbol{\Delta}$ or $\boldsymbol{\nabla}$ key.
- Depress SET key.
(ii) Conditions of transmission
- Key-in TYPE, 1 and SET.
- Select baud rate 4800 by using $\Delta$ or $\nabla$ key.
- Depress DEVICE/D key.
- Select bit configuration, 8NO1 (8-bit, no parity, 1 stop bit) by using $\Delta$ or $\boldsymbol{\nabla}$ key.
- Depress DEVICE/D key.
- Set to ENA (to perform XON/XOFF control) by using $\Delta$ or $\nabla$ key.
- Depress SET key.
(iii) Setting of transfer format
- Set the transfer format to intel-HEX.
- Key-in SELECT, 3 and SET.
- Select intel-HEX by using $\Delta$ or $\nabla$ key.
- Depress DEVICE/D key, and select terminator NON by using $\Delta$ or $\nabla$ key.
- Depress SET key.
(b) Connection of cable RS-232C (Cable length is around $3 \mathrm{~m})$


Note : RS-232C termination hand-shake is provided.
(c) Writing to P-ROM writer R4945
(ladder data/PC table data)
i. Connect the P-ROM writer (hereinafter called R4945) to the RS-232C interface of NC.
ii. Turn on the NC unit and switch to the JSD mode.
iii. Set the baud rate of the P-ROM writer ( 4800 bps ) to "09" according to the procedure of the parameter mode "JSD MODE 4" (4) on page 61.
iv. Return to the P-ROM writer mode of MODE 3. Viewing the CRT screen, note down the location numbers of the P-ROMs to write-in (\#30).
To write PC table data, depress input the P-ROM location number ( $\# 30$ ).
v. Turn on the R4945. (Transfer condition setting of R4945 in the above (a) should be completed before turning on the P-ROM writer.)
vi. Depress the 1 and $W R$ keys on the editing panel. (See Fig. 9.25)


Fig. 9.25
vii. Key-in a desired 2-digit P-ROM location number (noted numbers in procedure iv.) from editing panel. When the 3,0 and WR keys are keyed-in, display as shown in Fig. 9.26 will appear.


Fig. 9.26
viii. To complete receiving the serial data, depress the R4945 keys, SELECT, 0, SET, 0 and SET keys as this order.


Fig. 9.27
ix. Key in $R$ and $W R$ on editing panel. When $R$ key is depressed, buzzer in R4945 sounds as the response. Data is transferred from the SD to the R4945 and increase asterisks ( ${ }^{*}$ ) on the screen.

With steps i. through ix., data transfer from SD to R4945 and write-in to buffer RAM will have been completed. To transfer FC table data to R4945 after transferring ladder data to R4945, perform steps (iv) to (ix) again.
x. Set deleted P-ROM on R4945.
xi. To write-in to P-ROM, depress DEVICE, 4,

SET, DEVICE and SET keys as this order.
When write-in is completed, the sum value is displayed on R4945. Ladder data and PC table data are written in to \#30 PROM.
xii. To complete writing-in P-ROM of \#30, take off the written-in P-ROM from R4945 and store it.


R key on the editing panel is depressed.』


The response appears on the screen.


WR key on the editing panel is depressed.

## §



Data transfer is completed.

## $\Omega$



The response appears on the screen.
Fig. 9.28

### 9.10 PARAMETER MODE (MODE 4)

(1) CRT Display and Functions in Parameter Mode Operate the NEXT key to select MODE 4. The screen shown below will appear, displaying the functions available in this mode.

```
PARAMETER MODE 4
    FUNCTION 1--VERSION NO.
        2--TAPE COMAENT
        3--I/O DEFINE
        4--SYSTEM SAVE
        5--
        7--SYSTEM LOAD
        8--LADDER CLEAR
        9--SYSTEM RETLRN
        10--1/0 SELECT
    1234567:0013765
```

Fig. 9.29

### 9.10 PARAMETER MODE (MODE 4) <br> (Cont'd)

1. Version No. registration
2. Tape comment registration
3. Baud rate setting
4. Data transfer from DRAM to C-MOS
5. Not used
6. Not used
7. Data transfer from P-ROM to DRAM
8. Edit area clear
9. Reset to NC mode
10. I/O device selection

Keying-in one of the numbers ( 1 to 10 ) corresponding to the desired function selects that function. Given below is a detailed description of how each function can be utilized.
(2) Registering Version Number (1. VERSION NO.)

This function is used to register a sequence program version number. Be sure to register the number before writing to $\mathrm{P}-\mathrm{ROM}$.
The steps to do this are as follows:
(a) Operate the NEXT key to select MODE 4.
(b) Depress the 1 and WR keys.
(c) Key in a 7 -digit number for the desired version number.
(d) Depress the WR key. The 7-digit number will be registered as the version number.
The registered version number is displayed as shown in Fig. 9.30, upon applying power to the NC system.


Fig. 9.30

The high-order 5 digits are separated by a decimal point from the low-order 2 digits. What the digits signify for easiest identification is up to you.
(3) Registering Tape Comment
(2. TAPE COMMENT)

This function is used, upon punching out a P-ROM format tape or list tape, to punch a registered tape comment in perforated ornate characters following the feed hole portion.
The steps to make registration are as follows:
(a) Operate the NEXT key to select MODE 4.
(b) Depress the $2, W R$ key.
(c) Key-in a comment in 10 characters or less. The keys shown shaded in Fig. 9.30 are usable.
(d) Depress the WR key. The typed characters will be registered as the tape comment.


Typical Ornate Characters (10 characters or less in practice)

Fig. 9.31
(4) Setting Baud Rate (3. I/O DEFINE)

This function is used to match the baud rate of the JDU01 with the data transfer rate, or baud rate, of the RS-232C interface.
The steps to do this are as follows :
(a) Operate the NEXT key to select MODE 4.
(b) Depress the 3, WR key.
(c) Key in one of 2-digit numbers " 00 " to "19" that corresponds to the baud rate of the P-ROM writer. Refer to Table 9.3.
(d) Depress the WR key. The baud rate will be registered.

Table 9.3

| P-ROM Writer <br> Baud Rate | Key-Input Value |  |
| :---: | :---: | :---: |
|  | Data stop <br> signal <br> $=1$ bit | Data stop <br> signal <br> $=2$ bits |
| 50 | 00 | 10 |
| 100 | 01 | 11 |
| 110 | 02 | 12 |
| 150 | 03 | 13 |
| 200 | 04 | 14 |
| 300 | 05 | 15 |
| 600 | 06 | 16 |
| 1200 | 07 | 17 |
| 2400 | 08 | 18 |
| 4800 | $09^{*}$ | 19 |

* Baud rate "09" is automatically set when the JSD mode is entered. The rate remains unchanged if the above operations are not performed.

Note: Number of bits in data stop signal depends on P-ROM writer.
(5) Data transfer from RAM to C-MOS
(4. SYSTEM SAVE)

This function transfers the contents of an edit area (RAM) to a save area (CMOS).
The steps are as follows:
(a) Depress the NEXT key and select MODE 4.
(b) Depress [4 key and then WR key.
(c) Depress $L$ key and then WR key to save ladders. Depress $T$ key and then WR key to save tables.
(d) "SAVE END" will be displayed when the saving is completed. "SAVE ERROR" will be displayed when an error is detected. If an error is made then repeat from the step b.
(6) Data transfer from P-ROM to RAM and from C-MOS to RAM (7. SYSTEM LOAD)
This function transfers a sequence program which has been changed to a type of hardware by a P-ROM in a PC or a program which is stored in a C-MOS memory of the JDU01 into a RAM memory in the JDU01 (edit area). Operations should follow the steps shown below.
(a) By using the ROM/CMOS switch on the JDUO1, choose from which part (ROM or CMOS) the transfer to RAM is to be made.
(b) Depress NEXT key and select MODE 4.
(c) Depress 7 key and then WR key.
(d) Depress $L$ key and then WR key. The contents of the P-ROM or C-MOS is transferred to the edit area of the JDU01.
(e) For PC table, press $T$ key and then WR key.
(f) When the data transfer is completed, "LOAD END" will be displayed. When an error is made, "LOAD ERROR" will be displayed. If an error is made then restart from the step (c).
(7) Clearing of the edit area (8. LADDER CLEAR)

This function clears the edit area in the JDUOI (RAM memory) or the save area (C-MOS). Make sure to perform this operation loading a sequence program into the edit area for the first time in the SD mode or after replacing the battery. Following steps show the procedure.
(a) Depress the NEXT key and select the MODE 4.
(b) Depress (8) key and then WR key.
(c) Clear operation

For ladder clear: Depress the keys in the following order.
(i) C-MOS side $L, C, W R$
(ii) RAM side $T, T, W R$

For PC table: Press the keys in the following order.
(i) C -MOS side $\mathrm{T}, \mathrm{C}, \mathrm{WR}$
(ii) RAM side $T, R, W R$
(8) Return to the NC mode
(9. SYSTEM RETURN)

This function returns a mode from the XSD mode to the NC mode. This will be explained in the par. 9.13.
(9) Input/Output device selection
(10. I/O SELECT)

This function selects $1 / O$ port used in the $S D$ mode.
(a) Depress the NEXT key and select the MODE 4. (b) Depress 1 key, $O$ key, and then WR key.
(c) Depress $\square$ and then WR key. Here, the contents of $n$ is given by the Table 9.4. The initial value of $n$ when power is applied is zero. Once $n$ is determined, the value will be retained until power is turned off or the mode returns to the NC mode.

Table 9.4

| n | Input Device | Output Device |
| :--- | :---: | :---: |
| 0 | 1RO | 1RO |
| 1RO: IRS 232C |  |  |

### 9.11 PC DATA TABLE EDIT MODE (MODE 5)

Following operations can be done in this mode.
(1) Editing and address searching of PC data tables.
(2) Storing, verifying, and punching-out of P-ROM format tapes.

### 9.11.1 Editing of PC Data Tables

(1) CRT display in the MODE 5
(a) When the NEXT key is pressed and MODE 5 is selected, the CRT displays the following figure (shown in the Fig. 9.32).


Fig. 9.32
(b) Fix the SETTING to "1" by pressing 1 and WR. This operation makes the PC data table usable. When the table is not used, fix the SETTING to "0" by pressing $[0$ and the WR.
(c) Actual edit mode is given by depressing Pag key shown in Fig. 9.33.


Fig. 9.33
(2) Address search function

This function searches table numbers.
(a) Input a table number to be searched.

Example : By depressing $9,1,0,0$, the CRT displays 9100.
(b) Depress

key. The cursor moves
to the table number which has been searched.
(3) Key input operation
(a) Each data can be fit into a literal data or an ASCII code data. CST reads in input data at the HEX and displays them. ASC reads in input data as ASCII code and displays them. Anything which is not present in the ASCII code is displayed as "(a)." CST in Fig. 9.33 indicates that the data is currently a literal data. If the cursor is moved to this position and WR key is pressed, then ASC and CST can be changed alternately.
(b) The cursor is moved up and down.
(c) Insert mode is given by depressing INSERT key, and the cursor is moved to each data.
(d) Data can be changed in the insert mode.

## Example:

In case of literal data depress " 4 ". " 1 ", WR. In case of ASCII code data depress $A$, WR keys.
9.11.2 Reading-in, Punch-out, and Verify a P-ROM Format Tape (IN, OUT, and VER operations)
Like the ladder in the MODE 1 , this can be done by using $I N$, OUT, and VER keys. Refer to the P-ROM Format Tape I/O function in par. 9.7.3 for details.

### 9.12 ADDRESS CHECK MODE (MODE 6)

This function checks address duplications in the sequence ladder created by the JDU01.
(1) Check address area
\#1000 to \#1099 (Input from a machine)
\#1100 to \#1199 (Output from a machine)
\#1200 to \#1299 (Input from the NC)
\#1300 to \#1399 (Output from the NC)
\#1400 to \#1999 (Internal registers)
\#1700 to \#1799 (Timer)
\#7000 to \#7099 (Sequence parameter)
\#7100 to \#7999 (Keep memory area)
(2) Check operation

Number of "OUT \#xxxxx" will be counted in the sequence ladder.
(i) For \#1000's, \#1200's and \#1700's an address error will be displayed, if, for example, a command such as \#17521 (this address not an output address) can be found.
(ii) For \#l 100's, \#1300's from \#1400's to \#1900's, \#7000's and from \#7100 to \#7900, if, for example, more than two commands such as "OUT \#11112" can be found then an address error will be displayed.
(3) CRT display and its operation method
(a) When the NEXT key is pressed and MODE 6 is selected, the CRT displays Fig. 9.34.

| ADDRESS CHECK |  | MODE 6 |  |
| :---: | :---: | :---: | :---: |
|  |  |  |  |
| 0 | $\# 1000$ | 70 | $\# 7000$ |
| 1 | $\# 1100$ | 71 | $\# 7100$ |
| 2 | $\# 1200$ | 72 | $\# 7200$ |
| 3 | $\# 1300$ | 73 | $\# 7300$ |
| 4 | $\# 1400$ | 74 | $\# 7400$ |
| 5 | $\# 1500$ | 75 | $\# 7500$ |
| 6 | $\# 1600$ | 76 | $\# 7600$ |
| 7 | $\# 1700$ | 77 | $\# 7700$ |
| 8 | $\# 1800$ | 78 | $\# 7800$ |
| 9 | $\# 1900$ | 79 | $\# 7900$ |
| 10 | ALL ADDRESS |  |  |
|  |  |  |  |

Fig. 9.34
(b) Specify a number of a range to be checked. For example, if \#1300's (\#1300 to \#1399) will be checked then press 3: WR.
(c) When the above is keyed-in, the CRT displays the figure below (Fig. 9.35).


Fig. 9.35
"\#1300" shown above blinks.
In case of ALL ADDRESS CHECK, the screen continuously changes from \#1000.
(d) When checking is completed, the CRT displays Fig. 9.36 and Fig. 9.37.

| ADDRESS CHECK |  |
| :--- | :--- |
|  |  |
|  |  |
|  |  |

$=1300$ OK

Fig. 9.36

### 9.13 RETURN TO NC SYSTEM MODE (MODE 4)

The information that follows explains how to switch from the JDU01 editing mode to the NC system mode.
9.13.1 When NC Unit Entered SD Mode from Offline State

Do not return to the $N C$ mode if the $S D$ mode was entered by setting the System No, switch to 6 (See par. 9.6.1, When NC Unit is in Online State.)

After setting the sequence ladder to SAVE, be sure to turn off power. [For SAVE setting, see par. 9.10 (5).] When the edit area has been cleared in parameter mode, applying power supply again causes the NC mode to be entered.

Turn off power now even if a sequence program has already been edited.
9.13.2 When NC Unit Entered SD Mode from Online State

Operate the steps below if the SD mode was entered by setting the System No. switch to 4. (See par. 9.6.2 When NC Unit is in Online State.)
(a) Depress the NEXT key to select MODE 4.
(b) Press the 9 and WR key.
(c) Press the $N, C$ and $O R G$ keys, in that order. The system will be changed from the $S D$ mode to the NC mode.

Then setting the System No. switch to 0 or 4 in the NC mode enables operation check on the edited sequence program.

### 9.14 OPERATING PROCEDURE

Operating procedure for editing sequence program is shown in the flow chart below.


### 9.14 OPERATING PROCEDURE (Cont'd)

(2) Table 9.4 list the alarm codes at SD mode and operation for releasing them.

Table 9.5 Alarm Codes at SD Mode

| Alarm Code | Cause | $\begin{gathered} \text { CAN } \\ \text { key } \end{gathered}$ | RESET <br> key | Remarks |
| :---: | :---: | :---: | :---: | :---: |
| *ERRO01* | Wrong command or wrong setting keyed in. | $\bigcirc$ | $\bigcirc$ | - |
| *ERRO03* | Reading or punching error of P-ROM format tape | $\times$ | $\bigcirc$ | Alarms can be released by IN or VER key. |
| *ERRO08* | Address search unable | O | $\bigcirc$ | - |
| *ERR020* | Verifying error of list tape | $\times$ | $\bigcirc$ | Alarms can be released by VER or CURSOR key. |
| *ERR040* | Wrong input on MODE 3 | $\times$ | $\bigcirc$ | - - |
| *ERR050* | Table keyed-in not correct | 0 | $\bigcirc$ | - |
| *ERR051* | Table search unable | 0 | $\bigcirc$ | - |
| *DISASSEMBLE* | Memory contents not cleared | $\times$ | $\times$ | Alarms can be released by clearing MODE 4 edit area. |
| *MCR ERR* | Numbers of MCR and END are not same | $\times$ | $\times$ | Confirm numbers of MCR and END on MODE 1 |
| *LADDER FULL* | Exceeded memory capacity | $\bigcirc$ | 0 | Alarm occurs while list tape and MDI are stored. |
| *VER. ERR* | Verifying error of PROM format tape | $\times$ | O | Alarm can be released by VER key. |

O: Operating the key can release the alarm.
$\times$ : Operating the key cannot release the alarm.

## 10. SEQUENCE PROGRAM OFFLINE EDITING SYSTEM

This section describes the software to edit/create on a personal computer sequence programs which are operated by the NC unit YASNAC J50L or J50M and turn them into ROM. The software operating on a personal computer is called JSD offline system.

### 10.1 OUTLINE OF OFFLINE EDITING SYSTEM

JSD offline system calls four utility groups for YASNAC J50 PLC development which operate on MS-DOS. In order to create PLC ladder ROM, the JSD offline system has exclusive ladder use complier, linker, source converter and utility to turn into ROM.

### 10.1.1 Operation Environment

DOS : MS-DOS Ver3. 10 or above
Hardware : NEC PC-9801 series and IBM compatible machines (excluding LT and XL.)
Memory : Available memory exceeding 400 k -byte

### 10.1.2 Execution Files

The JSD offline system is composed of the following execution files.

|  | File Name | Explanation |
| :--- | :--- | :--- |
| Ladder Language <br> Compiler | JLCOMP.EXE | MS-DOS general purpose |
| Linker | JLLINK.EXE | MS-DOS general purpose |
| ROM Writer Output | JROMOUT.EXE | For PC9801 series |
|  | EJROMOUT.EXE | For IBM compatible <br> machines |
|  | XCONV.EXE | MS-DOS general purpose |

### 10.1.3 Outline of Execution File Processing

(1) Ladder language compiler

Compiles a source file which is coded in the ladder language and create a ROM-changed file.
The following shows data to be processed by compiler.

- Version Nos. (set at completion of linking)
- High-speed ladder programs
- Low-speed ladder programs
- Conversion data table
- Message data table
(2) Linker

Links an object file which is created by compiler.
(3) ROM writer output

Outputs a binary file from RS232C to the ROM writer by interl-HEX.

The following table shows capacity at which compiling is possible.

|  | Bytes | Approx. Number of <br> Steps Calculated | Number of PROMs |
| :---: | :---: | :---: | :---: |
| J 50 | 64 K | Approx. 16000 steps | 1024 k -bit. 1 unit |

The following table shows contacts and register numbers with which the compiler can compile.

|  | Item | Contact and Register No. |
| :---: | :---: | :---: |
| Address at which compiler can be converted | Input from machine | \#1000 to \#1061 |
|  | Output to machine | \#1100 to \#1155 |
|  | Input from NC | \#1200 to \#1295 |
|  | Output to NC | \#1300 to \#1329 |
|  | Internal relay address | $\begin{aligned} & \text { \#1400 to \#1699 } \\ & \text { \#1800 to } \# 1999 \end{aligned}$ |
|  | Timer address | \#1700 to \#1799 |
|  | Holding-type memory address | \#7000 to \#7999 (Including sequencer parameters) |
|  | Message table conversion | T9000 to T9023 T9025 to T9435 |

10.1.4 Outline of Operation
(1) Creating a source file in ladder language

Any editer which can create MS-DOS files can be used. To used them, create source files in ladder language. (For the details of ladder language format, refer to the description of compiler processing.)
The following shows typical creation of ladder source.

## YELADDER.SRC


10.1.4 Outline of Operation (Cont'd)
(2) Compiling created or modified file

Use JLCOMP to create an object file. (For the detailed operation, refer to the paragraph of compiler operation.)
(3) Collecting object files into one and creating a file to be executed

Use JLLINK to create a file to be executed. (For the detailed operation, refer to the paragraph of linker operation.)
In addition to when more than one objects are created, when all files are created with only one object, this linking processing is needed.
(4) Creating EPROM

When the resultant ladder execution check is successful, connect PC-9801 or IBM compatible machine to the ROM writer with RS-232C cable, and use JROMOUT to transfer the file to be executed to the ROM writer.

### 10.2 SOURCE FILE

The following describes the source file format to be input to the compiler.

### 10.2.1 Format of Source File

(1) Definition of character codes
(1) Any codes other than comments and character data must be ASCII codes. Both capital and small letters can be input. However, they cannot be identified in the internal processing. (They are identified as capital letters in the internal processing.)
Semi-block characters : 'aBc', 'a', 'Z'
Full-block characters : 'Character line', 'all'
(2) For comments, ASCII codes and SHIFT-JIS codes can be used.
(2) Definition of numerical values

Decimal notation : 9, 1234
Hexadecimal notation : $1234 \mathrm{H}, \mathrm{Oab} 12 \mathrm{H}, \mathrm{OFFH}$ *
Contact/ladder table No. : \#1000, \#10012, \#9024
Note : Any hexadecimal values starting with A from F must be added with 0 at the head of them.
(3) Types of pseudo-instructions

The following characters are processed as pseudo-instructions. These pseudo-instructions can be used only once in each source file.
highsequence
lowsequence
conversion
message
endp
include
(4) Nesting of source files

A source file of ladder program has considerable capacity so that it cannot be edited easily.
By providing include-file function in this compiling function, souce files which are divided individually (sequence source and table source) are collected into one to compile them.

LADDER.SRC


File nesting is enabled up to level 1 as shown above.

### 10.2.2 Source Files

The following describes the formats of source files, showing some examples.

YELADDER.SRC (Main file)

```
;************************************
; * J50 LADDER PROGRAM *
; ************************************
```

| (1) | HIGHSEQUENCE | ; HIGH-SPEED |
| :--- | :--- | :--- |
| (2) | INCLUDE LAD. HI | LADDER |
| (3) | ENDP |  |
| (4) | LOWSEQUENCE | ; LOW-SPEED |
|  | INCLUDE LAD.L01 | LADDER |
|  | INCLUDE LAD.L02 |  |
|  | INCLUDE LAD.L03 |  |
|  | ENDP |  |
| (5) | CONVERSION | ; CONVERSION |
|  | INCLUDE CONV.LAD | DATA |
|  | ENDP |  |
| (6) | MESSAGE | MESSAGE DATA |
|  | INCLUDE MESSAGE.DAT |  |
|  | ENDP |  |

(a) LAD.HI

(b) LAD.L01

(c) LAD.L02

(d) LAD.L03

(e) CONV.DAT


## (f) MESSAGE.DAT


(1) Name of source file

Any file name can be given freely.
$\qquad$ SRC
(2) Source file describing format

1. There is no limitation in character starting, lines, columns such as pseudo-instructions, sequence programs, data, etc.
2. Characters in the line after; are treated as a comment.
(3) Details of pseudo-instructions
(1) HIGHSEGUENCE

- Indicates the starting of high-speed ladder sequence.
- Sequence programs until ENDP are created as highspeed ladder for object.
- Format : HIGHSEQUENCE......ENDP
- High-speed ladder is not provided unless specified.
- It is necessary to write in this pseudo-instruction to the main file.
(2) INCLUDE
- Calls a file to be included.
- Format : INCLUDE

- It is possible to write in a pass name before the name of the file to be included.
INCLUDE B: $¥$ LPROG $¥$ LOW $¥ L A D . L 01$
(3) ENDP
- Indicates completion of high-speed ladder sequence, low-speed ladder sequence, conversion data or message data.
- Format : ENDP
- It is necessary to write in this pseudo-instruction to the main file.


## (4) LOWSEGUENCE

- Indicates the starting of low-speed ladder sequence.
- Sequence programs until ENDP are created as lowspeed ladder for object.
- Format: LOWSEQUENCE…..ENDP
- Low-speed ladder is not provided unless specified.
- It is necessary to write in this pseudo-instruction to the main file.


### 10.2.2 Source Files (Cont'd)

## (5) CONVERSION

- Created as ladder table data for object.
- Format : CONVERSION......ENDP
- Message data is not provided unless specified.
(6) MESSAGE
- Created as message table data for object.
- Format: MESSAGE......ENDP
- Message data is not provided unless specified.

Both pseudo-instruction, MESSAGE and CONVERSION have the same meaning. You can use conversion pseudoinstruction to define the message data in it, or vice versa. (Conversion data can be defined, too, in the message pseudo-instruction.)
(4) Include file

Pseudo-instructions, HIGHSEGUENCE, LOWSEGUENCE, CONVERSION and ENDP are to be written in to the main file.
(5) Each source file
(i) High-/low-speed ladder sequence files

- Write a sequence ladder program to be high-/low-speed processed.
- There is no limitation in character starting, lines or columns, expect that at least one space must be provided between each pseudo-instruction and address.
(ii) Conversion table/message table

Conversion table/message table files

- There is no limitation in data starting lines or colums, expect that at least one space must be provided between table No. and data.
- "T" must be added to the head of the table No.
- Each data item is divided with ",".
- Characters which can be defined as message data are semi-block ASCII codes.
- The following shows the table numbers to be used.

```
T9000 to T9007 : Up to 256 bytes -
T9008 to T9023: Up to 128 bytes
T9024 to T9087: Up to }64\mathrm{ bytes
T9088 to T9215 : Up to 32 bytes
T9216 to T9435 : Up to 16 bytes
version
tables, message tables
```

To store word data, underscore is added in front of each numerical value.

T9000 _1,_2,_3


- All data do not have to be written.

For example, when the number of conversion data items is 5 in SUBPOO7 instruction ;
T9000 ' $1,2,3,4,5$ '
The number of data items is 5 ; you do not have to write 256 items.
Omitted data are treated as 0 H .

### 10.3 COMPILER

### 10.3.1 Compiler Operation

Compiles created or corrected source files by JLCOMP instruction and creates object files.
The following describes how to start up JLCOMP.
JLCOMP [optional] FILE[.SRC][FILE2[.OBJ]]
[FILE3[.ERR][CR]

- Description of parameter

Option : Display language/E $\rightarrow$ Displayed in English
file 1 : Source file name (input)
file 2 : Object file name (output)
file 3 : Error file name (output)
Bracketed parts can be omitted.
When inputs of files 2 and 3 are omitted, default is set.

- When only JLCOMP is input, the parameter input guide is displayed.
- Example : JLCOMP B : LADTEST [CR]

If any error occurs, LADTEST.ERR is created. When no error occurs, LADTEST.OBJ is output.

- When the include function is used, only main file is compiled ; files to be included are compiled automatically.


### 10.3.2 Error List of Compile

Compiler outputs an error list file with extender as ERR in a file having the same name as that of the input file.
However, when any error file name is specified at activation of JLCOMP, a file having that name is output. Compile error information is stored in this error list file. When there is a filc having the same name as that of the error list file, that file is erased.

Error list file


### 10.3.3 Compiler Checking Items

Compiler checks that source format is to be processed. At the same time, it checks the following items.
(1) Command check
(1) Operation code check

OK : LD, LD-NOT, AND...
NG : ABS, XOR-NOT...
(2) Check of number of operands

OK : DEC \#1001, OFFH $\cdot$..
NG: DEC \#1001...
(3) Check of operand address specifying range

OK : LD \#10001…
NG: LD \#10…
(4) Check of operand constant specifying range

OK: MV1 \#1405, 55H
NG: MV1 \#1405, OFFFFH
(2) Check of upper/lower limit of number of characters set to ladder table
(3) Output contact check

- Checks that all output addresses of OUT instruction are unique.
- Checks the output contact address range.
(4) Check of MCR and END correspondence and lest level
(5) Timer check
- Checks the timer using register range.
- Checks that any timer (\#1700's) addresses are not overlapped.
(6) Check of STR (STR-NOT) and AND-STR (OR-STR) correspondence
(7) SUBP calling sequence check
- Checks that SUBP corresponds to PUSH (APSH, TPSH, IPSHD).
- Checks that SUBP corresponds to STR or STR-NOT.
(8) RTH and RET presence check
- Checks that there is one RTH.
- Checks that there is RET or RTI.


### 10.4 LINKER

Linker reads object files in the order which are indicated in the link module specification file, and performs processing in which the data contained in the files are mapped into the executable file in the same format as that of ladder ROM.
Linker performs linking processing for the following three objects.
(1) Ladder program object
(2) Table related object
(conversion table and message table)

* More than one object file of the ladder program is not allowed.
10.4.1 Object Data and Linker Processing

The following describes the linker processing for data contained in object files.
(1) High-speed data (highsequence setting data)

- Checks the maximum range of the ladder storing area.
- An error occurs when there is no RTH.
(2) Low-speed ladder data (lowsequence setting data)
- Checks the maximum range of the ladder storing area.
- An error occurs when there is no RET or RTI.
(3) Table data
(CONVERSION/MESSAGE setting data)
- Stores message data to specify addresses corresponding to variables T9000 to T9435.
- Generates an error when the same variable data exist in some object files.


### 10.4.2 Linker Operation

Changes an object file output by compiler to a link binary file by JLLINK instruction.
(1) Link module file

It is necessary to create a link module file before activation of JLLINK.
By using this file, an object file to be linked is specified.
(A) Name of link module file

FILE1.LNK
Any file name can be given freely, however, the name of the extender must be always LNK.
(B) Format of link module file

- All object files to be linked are specified as shown below.
- There is not limitation in character starting line or column. (The number of characters in one line including pass is up to 80 .)
- Link module can be specified within 80 characters in one column including pass.
- High-/low-speed ladders must be actually executed in the order of specification in this file.
10.4.2 Linker Operation (Cont'd)

YELAD.LNK

```
LADSRC.OBJ
LADCNV.OBJ
LADMSG.OBJ
```

(2) How to activate JLLINK

## JLLINK FILE1.LNK [FILE2][CR]

- Description of parameter

FILE1: Name of link module specifying file (input)
FILE2 : Name of binary file (output)
Bracketed parts can be omitted.
When FILE2 is omitted, the name of it will be the same as that of FILE1.

- When only JLLINK is input, the parameter input guide is displayed.
(3) Input of version No.

When a link completes successfully, a version No. can be input.
Linker motions version No. input.
The inputting range of version numbers is indicated as 7 digit value.
Since the upper 5 digits and lower 2 digits are registered separately with a decimal point, pay attention to the meaning.


REGISTERED AS 12345.67

### 10.4.3 Linker Output File

The result of the linking by JLLINK is created as a binary output file.
Example : JLLINK YELAD.LNK[CR]
Output file
YELAD.BIN Ladder execution file

- Ladder execution file

A binary file including codes where actual ladder codes are turned into assembler.

### 10.5 CHANGING INTO ROM

### 10.5.1 Selection of PROM Writer

The user is expected to prepare a commercially available P-ROM writer with the following 4 features:
(1) Reading in the "INTEL HEX Format" is available for data transfer.
(2) Writing to the P-ROM 271024 (INTEL system) is available.
(3) The RS232C interface is provided.

The following are some recommended P-ROM writers that meet the above requirements.

AF-9704 EPROM PROGRAMMER made by ANDO DENKI CO., LTD. EPROM PROGRAMMER R4945 made by ADOBANSUTO INC.

Ladder ROM can be created by using personal computer RS232C.

### 10.5.2 Line Connection

The following shows connection of personal computer and ROM writer.


### 10.5.3 Transfer Parameters

Transfer parameters at the PROM writer and personal computer sides must be set as follows.
(The following setting shows some recommended values. Any settings can be made only if settings of the personal computer and the PROM writer are the same.)
Baud rate : 9600 bps
Data bit : 8 bits
Parity : None
Stop bit : 2 bits
XON/XOFF : ON
(1) Set the PROM writer to the receiving status and input A: $¥>$ JROMOUT YELAD.BIN.
The display indicates that the data are being transferred and transfer starts.

```
A:¥>JROMOUT YELAD.BIN
JROMOUT Ver1.0
```


## EXECUTING

(2) When transfer is completed, the main menu is returned again.

### 10.6 JSD LADDER SOURCE CONVERTER

Source programs or table programs output by JSD can be converted into a format where compiling is enabled by JLCOMP.

Using method

- Display of using method

By executing without inputting a file name, the using method is displayed.
X/J Ladder Source Converter xconv Ver 1.0
Copyright Yaskawa Electric
USAGE XCONV [file1] [file2]
file1: input file
file2 : output file

- Conversion of source/table files

To convert a source file named LAD.SRC into LAD2.SRC, perform the following steps.
A: $¥>$ XCONV LAD.SRC LAD2.SRC
To convert a table file named LAD.TBL into LAD2.TBL, perform the following steps.
A: $¥>$ XCONV LAD.TBL LAD2.TBL

The source file from which conversion is made must be the same as two files (source/table) output by JSD.
Source file and table file must be different from each other.

### 10.7 LIST OF ERROR MESSAGES AND WARNING MESSAGES

The following outlines errors and warnings that compiler or linker generates.
Normally, the error file is created in Japanese. Therefore, to create it in English, add switch "/E" at compiling.
$/ * * * * * *<$ error message> $* * * * * * * * * * * * * /$
1-line characters over
Illegal character is used.
Over the nest of source-file
Illegal character is used instead of pseudo-instruction.
A pseudo-instruction is used duplicatedly.
'ENDP' cannot be found.
Characters of a word is too long.
Invalid operator
Object-file memory size over
Operand of an instruction is not enough.
Operand-address is not correct.
Operand-byte-data is not correct.
Operand-word-data is not correct.
SUBP number is not correct.
Table-number define error
Table-number-setting-range is not correct.

Character data define error
Character data range define error
Character data lines over
Variable number error
Out instruction address range over
Timer-register range error
Number of MCR \& END is unmatch
Byte data define error
Word data define error
Data data define error
Data range define error
Number of Operands are too large, or include valid characters.
Nest of MCR over
Duplicatedly use of valiable number
SUBP calling sequence error
Number of USBP \& PUSH is unmatch
Nest of STR over
Number of stack instruction by STR is not correct.
SUBP023 parameter error $¥ Y n ¥ 0^{\prime \prime}$
/****** <warning message> *************/
[WARNING] Out instruction Already Define.
[WARNING] Timer address already Define.

### 10.8 NOTES

The number of object files to be created must be less than the following three types.

- Ladder source object
- Table related object : Conversion table Message table

When more than one ladder source files is provided, use INCLUDE pseudo-instruction to create them as one object at compiling.

## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES)

This I/O list shows the following I/O board composition.
List No. 1 : Standard I/O board (JANCD-FC810, FC860)
List No. 2 : CRT panel built-in 1/O board (JANCD-SP50)

## - <Input from Machine >

\# 1000

| D 7 | D | D | D 4 | D 3 | D 2 | D 1 | D 0 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $04-36$ | $04-21$ | $04-05$ | $04-35$ | $04-20$ | $04-34$ | $04-19$ | $04-33$ |

\# 1001

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-24$ | $04-08$ | $04-38$ | $04-23$ | $04-07$ | $04-37$ | $04-22$ | $04-06$ |

\# 1002

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-11$ | $04-41$ | $04-26$ | $04-10$ | $04-40$ | $04-25$ | $04-09$ | $04-39$ |

\# 1 003

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-45$ | $04-14$ | $04-44$ | $04-13$ | $04-43$ | $04-12$ | $04-42$ | $04-27$ |

\# 1004

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-49$ | $04-18$ | $04-48$ | $04-17$ | $04-47$ | $04-16$ | $04-46$ | $04-15$ |

\# 1005

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-06$ | $05-07$ | $05-38$ | $05-39$ | $05-20$ | $05-21$ | $05-22$ | $05-23$ |

\# 1006

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-08$ | $05-09$ | $05-40$ | $05-10$ | $05-24$ | $05-25$ | $05-11$ | $05-12$ |

\# 1007

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-13$ | $05-37$ | $05-05$ | $05-14$ | $05-15$ | $05-16$ | $05-17$ | $05-18$ |

\#1008

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-41$ | $05-26$ | $05-27$ | $05-19$ | $05-33$ | $05-34$ | $05-35$ | $05-36$ |

\# 1009

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-42$ | $05-43$ | $05-44$ | $05-45$ | $05-46$ | $05-47$ | $05-48$ | $05-49$ |

\# 1010

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-11$ | $03-41$ | $03-26$ | $03-10$ | $03-40$ | $03-25$ | $03-09$ | $03-39$ |

\# 1011

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-45$ | $03-14$ | $03-44$ | $03-13$ | $03-43$ | $03-12$ | $03-42$ | $03-27$ |

\# 1012

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-49$ | $03-18$ | $03-48$ | $03-17$ | $03-47$ | $03-16$ | $03-46$ | $03-15$ |

## - < Input from Machine >

\# 1013

| D7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $02-16$ | $02-09$ | $02-03$ | $02-15$ | $02-08$ | $02-02$ | $02-14$ | $02-01$ |

\# 1016

| $\cdot$ |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $04-08$ | $04-07$ | $04-06$ | $04-05$ | $04-04$ | $04-03$ | $04-02$ | $04-01$ |

\# 1017

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-16$ | $04-15$ | $04-14$ | $04-13$ | $04-12$ | $04-11$ | $04-10$ | $04-09$ |

\# 1018

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-26$ | $04-25$ | $04-24$ | $04-23$ | $04-22$ | $04-21$ | $04-20$ | $04-19$ |

\# 1019

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $05-08$ | $05-07$ | $05-06$ | $05-05$ | $05-04$ | $05-03$ | $05-02$ | $05-01$ |

\# 1020

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $05-16$ | $05-15$ | $05-14$ | $05-13$ | $05-12$ | $05-11$ | $05-10$ | $05-09$ |

\#1021

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-26$ | $05-25$ | $05-24$ | $05-23$ | $05-22$ | $05-21$ | $05-20$ | $05-19$ |

\# 1022

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $06-08$ | $06-07$ | $06-06$ | $06-05$ | $06-04$ | $06-03$ | $06-02$ | $06-01$ |

\#1023

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-16$ | $06-15$ | $06-14$ | $06-13$ | $06-12$ | $06-11$ | $06-10$ | $06-09$ |

## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) (Cont'd)

-(O) <Output to Machine>
\# 1100

| D7 | D6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $01-05$ | $01-06$ | $01-07$ | $01-08$ | $01-41$ | $01-27$ | $01-26$ | $01-25$ |

\# 1101

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-09$ | $01-10$ | $01-19$ | $01-20$ | $01-21$ | $01-22$ | $01-23$ | $01-24$ |

\# 1102

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-33$ | $01-34$ | $01-35$ | $01-36$ | $01-37$ | $01-38$ | $01-39$ | $01-40$ |

\# 1103

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $01-11$ | $01-12$ | $01-13$ | $01-14$ | $01-15$ | $01-16$ | $01-17$ | $01-18$ |

\# 1104

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-42$ | $01-43$ | $01-44$ | $01-45$ | $01-46$ | $01-47$ | $01-48$ | $01-49$ |

\#1105

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $02-07$ | $02-12$ | $02-06$ | $02-11$ | $02-05$ | $02-17$ | $02-10$ | $02-04$ |

\# 1106

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-36$ | $03-21$ | $03-05$ | $03-35$ | $03-20$ | $03-34$ | $03-19$ | $03-33$ |

\# 1107

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-24$ | $03-08$ | $03-38$ | $03-23$ | $03-07$ | $03-37$ | $03-22$ | $03-06$ |

\# 1108

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-11$ | $06-12$ | $06-13$ | $06-14$ | $06-15$ | $06-16$ | $06-17$ | $06-18$ |

\# 1109

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-42$ | $06-43$ | $06-44$ | $06-45$ | $06-46$ | $06-47$ | $06-48$ | $06-49$ |

\# 1110

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-19$ | $06-20$ | $06-21$ | $06-22$ | $06-23$ | $06-24$ | $06-25$ | $06-26$ |

\# 1111

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-33$ | $06-34$ | $06-35$ | $06-36$ | $06-37$ | $06-38$ | $06-39$ | $06-40$ |

-(O)- <Output to Machine >
D7 7 D6
\# 1116

| D7 | D 6 | D5 | D 4 | D3 | D2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $04-34$ | $04-33$ | $04-32$ | $04-31$ | $04-30$ | $04-29$ | $04-28$ | $04-27$ |

\# 1117

|  |  |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $04-42$ | $04-41$ | $04-40$ | $04-39$ | $04-38$ | $04-37$ | $04-36$ | $04-35$ |

\# 1118

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-34$ | $05-33$ | $05-32$ | $05-31$ | $05-30$ | $05-29$ | $05-28$ | $05-27$ |

\# 1119

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-42$ | $05-41$ | $05-40$ | $05-39$ | $05-38$ | $05-37$ | $05-36$ | $05-35$ |

* For JANCD-SP-50-2, 24 points shown below are effective.
\# 1120

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-26$ | $06-25$ | $06-24$ | $06-23$ | $06-22$ | $06-21$ | $06-20$ | $06-19$ |

\# 1121

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-34$ | $06-33$ | $06-32$ | $06-31$ | $06-30$ | $06-29$ | $06-28$ | $06-27$ |


|  | 1199. |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | | $\#$ |  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-24$ | $03-08$ | $03-38$ | $03-23$ | $03-07$ | $03-37$ | $03-22$ | $03-06$ |  |

\#1108

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-11$ | $06-12$ | $06-13$ | $06-14$ | $06-15$ | $06-16$ | $06-17$ | $06-18$ |

\# 1109

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-42$ | $06-43$ | $06-44$ | $06-45$ | $06-46$ | $06-47$ | $06-48$ | $06-49$ |

\# 1110

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-19$ | $06-20$ | $06-21$ | $06-22$ | $06-23$ | $06-24$ | $06-25$ | $06-26$ |

\# 1111

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-33$ | $06-34$ | $06-35$ | $06-36$ | $06-37$ | $06-38$ | $06-39$ | $06-40$ |

## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) (Cont'd)

——— Input from NC >
\# 1200


M FUNCTION BCD OUTPUT

| \#1201 | M30R | M02R | M01R | M00R | M38 | M34 | M32 | M31 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | M30 DECODE OUTPUT | M02 <br> DECODE <br> OUTPUT | M01 <br> DECODE OUTPUT | m00 DECODE OUTPUT |  |  |  |  |
| \#1202 | TF | SF | MF | SINVA | IER | *ESPS | RST | ALM |
|  | $\begin{aligned} & \text { T-FUNC- } \\ & \text { TION } \\ & \hline \end{aligned}$ | $\underbrace{\begin{array}{l} \text { S-FUNC- } \\ \text { TION } \end{array}}$ | $\begin{aligned} & \text { M-FUNC- } \\ & \text { TION } \\ & \hline \end{aligned}$ | S-4 DIGIT OUT | INPUT ERROR OUTPUT | EMER- <br> GENCY <br> STOP | RESET oUTPUT | ALARM OUTPUT |
|  |  | upLIng O | PUT | $\begin{aligned} & \text { INVERT } \\ & \text { STATUS } \end{aligned}$ |  | OUTPUT |  |  |
| \#1203 |  | EDTS | AUTO | MAN | THC | RWDS | OP | DEN |
|  |  | EDIT <br> OPERAT- <br> ING <br> STATUS | $\begin{aligned} & \text { AUTO } \\ & \text { MODE } \\ & \text { STATUS } \end{aligned}$ | MANUAL MODE STATUS | THREAD CUTTING STATUS | $\begin{aligned} & \text { REWIND } \\ & \text { STATUS } \end{aligned}$ | FEEDING | POSI- <br> TIONING <br> END |


\#1204 | S 28 | S 24 | S 22 | S 21 | S 18 | S 14 | S 12 | S 11 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

S FUNCTION BCD OUTPUT

\#1205 | T 28 | T 24 | T 22 | T 21 | T 18 | T 14 | T 12 | T 11 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

T FUNCTION BCD OUTPUT

$\square$
 EXTERNAL OUTPUT FOR S-COMMAND (S4 DIGIT) NO. 1
\#1217

| (SDO15) | (SDO14) | (SDO13) | (SDO12) | R012(SDO11) | R011(SDO10) | R010(SD0 9) | R09 (SDO8) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |



## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) (Cont'd)




\#1285 | 0 |
| :---: |
| CONSTANT " $1 "$ |


| \#1286 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | CONSTANT "0" |  |  |  |  |

\#1287

\#1289

| TGONZ | PCZ | PBZ | PAZ | * ALZ | * OLZ | FUZ | SRDZ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| z-AXIS | $\underbrace{\text { PHASE-C }}$ PHASE-B PHASE-A |  |  | MONITOR FOR SERVO |  | UNIT | OF Z-AXIS |
| TG ON |  |  |  |  |  |  |  |


\#1290 |  | SCOM28 | SCOM24 | SCOM22 | SCOM21 | SCOM18 | SCOM14 | SCOM12 | SCOM11 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

SPINDLE COMMAND MONITOR

\#1291 | SCOM48 | SCOM44 | SCOM42 | SCOM41 | SCOM38 | SCOM34 | SCOM32 | SCOM31 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

SPINDLE COMMAND MONITOR

| \#12 | S 24 | SO28 | SO24 | SO22 | SO21 | SO18 | SO14 | SO12 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | SO11 |  |  |  |  |  |  |  |



SPINDLE OUTPUT MONITOR

\#1294 | ALM 28 | ALM 24 | ALM 22 | ALM 21 | ALM 18 | ALM 14 | ALM 12 | ALM 11 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

ALARM CODE MONITOR


\#1296 | INHEDTT | AFLT | ABST | DRNT | BDTT | DLRT | MLKT |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) (Cont'd)

- O- <output to NC >

| \#1300 | D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | EDT | MEM | D | T |  | H/ S | J | RT |
|  | EDIT | MEMORY | MDI | TAPE |  | HANDI STEP | $\begin{aligned} & \text { MANU } \\ & \text { JOG } \end{aligned}$ | MANUAL RAPID |

\#1301

| MP 1 | ROV 2 | ROV 1 | FV16 | FV 8 | FV 4 | FV 2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MANUAL <br> PG MUL- RAPID SPEED OVERRIDE <br> TIPLE <br> SELECT |  |  |  |  |  |  |

\# 1302

| HZ | HX | -Z | +Z | -X | +X | MP 4 | MP 2 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MANUAL PG AXIS SELECT |  | MANUAL TRAVERSE AXIS DIRECTION SELECT |  |  |  | MANUAL PG mULTIPLY SELECT |  |


| \#1303 | INHEDT | AFL | ABS | DRN | BDT | DLK | MLK | SBK |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | INHIBIT EDIT | $\begin{aligned} & \text { M.S.T } \\ & \text { LOCK } \end{aligned}$ | MANUAL ABS . | DRY RUN | BLOCK DELETE | DISPLAY | MACHINE LOCK | SINGLE |
|  |  |  | ABS. |  | DELETE | LOCK | LOCK | BLOCK |

\#1304

| ZRN | CDZ | SMZ | RWDH | SRN | PST | * SP | ST |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RETURN | THREAD | ERROR | HIGH | SET UP | POSITIION | FEED | CYCLE |
| TO REF- | CUT UP | DETECT | SPEED | POINT | SET | HOLD | START |
| ERENCE |  |  | REWIND | RETURN |  |  |  |


| \#1305 | ERR 1 | ERR 0 | STLK | RWD | EOP | ERS | FIN | MRD |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | EXTERNAL ERRORINPUT |  | INTER- <br> RUPT | REWIND | END OF PROGRAM | EXTERNAL RESE'T | MST FIN | MACHINE READY |

\#1306

| SAGR | $* \operatorname{DCZ}$ | $* \mathrm{DCX}$ | $*-\mathrm{LZ}$ | $*+\mathrm{LZ}$ | $*-\mathrm{LX}$ |
| :--- | :--- | :--- | :--- | :--- | :--- |
| SPINDLE <br> SPEED <br> AGREE- <br> MENT | $\underbrace{}_{\substack{\text { DECREASE INPUT FOR } \\ \text { REFERENCE POINT }}}$ | $*+\mathrm{LX}$ |  |  |  |

\#1307

| GRS | GSC | SSTP | SINV | GR 4 | GR 3 | GR 2 | GR 1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| S- | SPINDLE | S- | S- |  |  |  |  |
| COMMAND | SPEED | COMMAND | COMMAND | SPINDLE GEAR RANGE SELECT |  |  |  |
| CONSTANT | CONSTANT | "0" | INVERT |  |  |  |  |

\#1308

| EOUT | EVER | EIN | DRSZ | DRSX |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :---: |
| NC NC NC  <br> PROGRAM PROGRAM PROGRAM DISPLAY RESET <br> PUNCH VERIFY INPUT TIME <br> OUT    |  |  |  | COUNT |  |  |

\#1309

| BDT 9 | BDT 8 | BDT 7 | BDT 6 | BDT 5 | BDT 4 | BDT 3 | BDT 2 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

ADDITIONAL BLOCK DELETE

\#1321 $\square$

## APPENDIX 1 I/O LIST FOR YASNAC J50L (FOR LATHES) (Cont'd)



## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS)

This I/O list shows the following I/O board composition.
List No. 1: Standard mounted I/O board (JANCD-FC810, FC860)
List No. 2 : Standard mounted I/O board (JANCD-FC810, FC860)

| \# 1 0 0 0 | D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 04-36 | 04-21 | 04-05 | 04-35 | 04-20 | 04-34 | 04-19 | 04-33 |
| \# 1 0 0 1 |  |  |  |  |  |  |  |  |
|  | 04-24 | 04-08 | 04-38 | 04-23 | 04-07 | 04-37 | 04-22 | 04-06 |

\# 1002

|  |  |  | . |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-11$ | $04-41$ | $04-26$ | $04-10$ | $04-40$ | $04-25$ | $04-09$ | $04-39$ |

\#1003

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-45$ | $04-14$ | $04-44$ | $04-13$ | $04-43$ | $04-12$ | $04-42$ | $04-27$ |

\#1004

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-49$ | $04-18$ | $04-48$ | $04-17$ | $04-47$ | $04-16$ | $04-46$ | $04-15$ |

\# 1005

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-06$ | $05-07$ | $05-38$ | $05-39$ | $05-20$ | $05-21$ | $05-22$ | $05-23$ |

\# 1006

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-08$ | $05-09$ | $05-40$ | $05-10$ | $05-24$ | $05-25$ | $05-11$ | $05-12$ |

\# 1007

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-13$ | $05-37$ | $05-05$ | $05-14$ | $05-15$ | $05-16$ | $05-17$ | $05-18$ |

\# 1008

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-41$ | $05-26$ | $05-27$ | $05-19$ | $05-33$ | $05-34$ | $05-35$ | $05-36$ |

\# 1009

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-42$ | $05-43$ | $05-44$ | $05-45$ | $05-46$ | $05-47$ | $05-48$ | $05-49$ |

\# 1010

|  |  |  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-11$ | $03-41$ | $03-26$ | $03-10$ | $03-40$ | $03-25$ | $03-09$ | $03-39$ |

\#1011

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-45$ | $03-14$ | $03-44$ | $03-13$ | $03-43$ | $03-12$ | $03-42$ | $03-27$ |

\# 1012

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-49$ | $03-18$ | $03-48$ | $03-17$ | $03-47$ | $03-16$ | $03-46$ | $03-15$ |

\#1013
(For special application)

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $02-16$ | $02-09$ | $02-03$ | $02-15$ | $02-08$ | $02-02$ | $02-14$ | $02-01$ |

## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)

- 1 -Input from Machine>

| \#1016 | D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  |
|  | 04-36 | 04-21 | 04-05 | 04-35 | 04-20 | 04-34 | 04-19 | 04-33 |

\# 10017

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-24$ | $04-08$ | $04-38$ | $04-23$ | $04-07$ | $04-37$ | $04-22$ | $04-06$ |

\# 1018

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-11$ | $04-41$ | $04-26$ | $04-10$ | $04-40$ | $04-25$ | $04-09$ | $04-39$ |

\# 1019

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-45$ | $04-14$ | $04-44$ | $04-13$ | $04-43$ | $04-12$ | $04-42$ | $04-27$ |

\# 1020

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $04-49$ | $04-18$ | $04-48$ | $04-17$ | $04-47$ | $04-16$ | $04-46$ | $04-15$ |

\# 1021

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-06$ | $05-07$ | $05-38$ | $05-39$ | $05-20$ | $05-21$ | $05-22$ | $05-23$ |

\# 1022

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-08$ | $05-09$ | $05-40$ | $05-10$ | $05-24$ | $05-25$ | $05-11$ | $05-12$ |

\# 1023

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $05-13$ | $05-37$ | $05-05$ | $05-14$ | $05-15$ | $05-16$ | $05-17$ | $05-18$ |

\# 1024

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-41$ | $05-26$ | $05-27$ | $05-19$ | $05-33$ | $05-34$ | $05-35$ | $05-36$ |

\# 1025

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $05-42$ | $05-43$ | $05-44$ | $05-45$ | $05-46$ | $05-47$ | $05-48$ | $05-49$ |

\# 1026

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-11$ | $03-41$ | $03-26$ | $03-10$ | $03-40$ | $03-25$ | $03-09$ | $03-39$ |

\#1027

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-45$ | $03-14$ | $03-44$ | $03-13$ | $03-43$ | $03-12$ | $03-42$ | $03-27$ |

\# 1028

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-49$ | $03-18$ | $03-48$ | $03-17$ | $03-47$ | $03-16$ | $03-46$ | $03-15$ |

\# 1029

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $02-16$ | $02-09$ | $02-03$ | $02-15$ | $02-08$ | $02-02$ | $02-14$ | $02-01$ |

-(O) <Output to Machine>

| D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $01-05$ | $01-06$ | $01-07$ | $01-08$ | $01-41$ | $01-27$ | $01-26$ | $01-25$ |

\#1101

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-09$ | $01-10$ | $01-19$ | $01-20$ | $01-21$ | $01-22$ | $01-23$ | $01-24$ |

\# 1102

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $01-33$ | $01-34$ | $01-35$ | $01-36$ | $01-37$ | $01-38$ | $01-39$ | $01-40$ |

\# 1103

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-11$ | $01-12$ | $01-13$ | $01-14$ | $01-15$ | $01-16$ | $01-17$ | $01-18$ |

\# 1104

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-42$ | $01-43$ | $01-44$ | $01-45$ | $01-46$ | $01-47$ | $01-48$ | $01-49$ |

\#1105

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $02-07$ | $02-12$ | $02-06$ | $02-11$ | $02-05$ | $02-17$ | $02-10$ | $02-04$ |

\# 1106

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $03-36$ | $03-21$ | $03-05$ | $03-35$ | $03-20$ | $03-34$ | $03-19$ | $03-33$ |

\# 1107

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-24$ | $03-08$ | $03-38$ | $03-23$ | $03-07$ | $03-37$ | $03-22$ | $03-06$ |

\# 1108

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-11$ | $06-12$ | $06-13$ | $06-14$ | $06-15$ | $06-16$ | $06-17$ | $06-18$ |

\# 1109

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-42$ | $06-43$ | $06-44$ | $06-45$ | $06-46$ | $06-47$ | $06-48$ | $06-49$ |

\# 1110

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-19$ | $06-20$ | $06-21$ | $06-22$ | $06-23$ | $06-24$ | $06-25$ | $06-26$ |

\# 1111

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-33$ | $06-34$ | $06-35$ | $06-36$ | $06-37$ | $06-38$ | $06-39$ | $06-40$ |

## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)

-(O- <Output to Machine >

| D 6 | D 5 | D 4 | D 3 | D2 | D 1 | D 0 |  |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
| $01-05$ | $01-06$ | $01-07$ | $01-08$ | $01-41$ | $01-27$ | $01-26$ | $01-25$ |

\# 1117

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $01-09$ | $01-10$ | $01-19$ | $01-20$ | $01-21$ | $01-22$ | $01-23$ | $01-24$ |

\# 1118

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $01-33$ | $01-34$ | $01-35$ | $01-36$ | $01-37$ | $01-38$ | $01-39$ | $01-40$ |

\# 11.19

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-11$ | $01-12$ | $01-13$ | $01-14$ | $01-15$ | $01-16$ | $01-17$ | $01-18$ |

\# 1120

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $01-42$ | $01-43$ | $01-44$ | $01-45$ | $01-46$ | $01-47$ | $01-48$ | $01-49$ |

\# 1121

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $02-07$ | $02-12$ | $02-06$ | $02-11$ | $02-05$ | $02-17$ | $02-10$ | $02-04$ |

\#1122

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-36$ | $03-21$ | $03-05$ | $03-35$ | $03-20$ | $03-34$ | $03-19$ | $03-33$ |

\#1123

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $03-24$ | $03-08$ | $03-38$ | $03-23$ | $03-07$ | $03-37$ | $03-22$ | $03-06$ |

\#1124

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-11$ | $06-12$ | $06-13$ | $06-14$ | $06-15$ | $06-16$ | $06-17$ | $06-18$ |

\#1125

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-42$ | $06-43$ | $06-44$ | $06-45$ | $06-46$ | $06-47$ | $06-48$ | $06-49$ |

\#1126

|  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $06-19$ | $06-20$ | $06-21$ | $06-22$ | $06-23$ | $06-24$ | $06-25$ | $06-26$ |

\#1127

|  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $06-33$ | $06-34$ | $06-35$ | $06-36$ | $06-37$ | $06-38$ | $06-39$ | $06-40$ |



## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)


\# 121

\# 1212

\#1213 $\square$
\#1214 $\square$
\#1215



T-FUNCTION BINARY/BCD OUTPUT

| \# 1217 | $\mathrm{~T} 16 / \mathrm{T} 48$ | $\mathrm{~T} 15 / \mathrm{T} 44$ | $\mathrm{~T} 14 / \mathrm{T} 42$ | $\mathrm{~T} 13 / \mathrm{T} 41$ | $\mathrm{~T} 12 / \mathrm{T} 38$ | $\mathrm{~T} 11 / \mathrm{T} 34$ | $\mathrm{~T} 10 / \mathrm{T} 32$ | $\mathrm{~T} 9 / \mathrm{T} 31$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

T-FUNCTION BINARY/BCD OUTPUT
\#1228 $\square$

## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)

## - - Input from NC >


\#1230 $\square$ I $\square$ I $\quad$ I $1 \square$
\# 1231 $\square$

\#1232 | \# $8 / \mathrm{B} 28$ | $\mathrm{~B} 7 / \mathrm{B} 24$ | $\mathrm{~B} 6 / \mathrm{B} 22$ | $\mathrm{~B} 5 / \mathrm{B} 21$ | $\mathrm{~B} 4 / \mathrm{B} 18$ | $\mathrm{~B} 3 / \mathrm{B} 14$ | $\mathrm{~B} 2 / \mathrm{B} 12$ | $\mathrm{~B} 1 / \mathrm{B} 11$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | B-FUNCTION BINARY/BCD OUTPUT


| \#1233 | B16/B48 | B15/B44 | B14/B42 | B13/B41 | B12/B38 | B11/B34 | B10/B32 | B 9 /B31 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | B-FU | CTION BIM | RY/BCD O | Put | $\begin{aligned} & \text { HIGH- } \\ & \text { SPEED } \\ & \text { GEAR } \end{aligned}$ | $\begin{aligned} & \text { LoW- } \\ & \text { SPEED } \\ & \text { GEAR } \end{aligned}$ |
| \#1234 | S28 | S24 | S22 | S21 | S18 | S14 | S12/GRH | S11/GRL |


\#1235 | S48 | S 44 | S 42 | S 41 | S 38 | S 34 | S 32 | S 31 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

S-FUNCTION BCD OUTPUT

| \# 1236 | U 7 | U 6 | U 5 | U 4 | U 3 | U 2 | U 1 | U 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

MACRO PROGRAM
\#1237

| U15 | U14 | U13 | U12 | U11 | U10 | U9 | U8 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

MACRO PROGRAM

- < Input from NC >


\#1239 |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |


\#1278


\#1279 |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |




\#1283


## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)


\#1285


CONSTANTS "1"
\#1286


CONSTANTS "0"
\#1287
 SPINDLE PG MONITOR

\#128

\#129

| TGONZ | PCZ | PBZ | PAZ | $* A L Z$ | $*$ OLZ | FUZ | SRDZ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $Z-A X I S ~ P G ~ M O N I T O R ~$ | Z-AXIS SERVO UNIT MONITOR |  |  |  |  |  |  |

\#1291

\#1292 $\square$ $1 \quad 1$ $\square$
$\square$
$\square$


## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)


\#1301

| OVAC | ROV 2 | ROV 1 | OV16 | OV 8 | OV 4 | OV 2 | OV 1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CANCEL |  |  |  |  |  |  |  |
| RAPID TRAVERSE <br> RATE OVERRIDE |  | FEEDRATE OVERRIDE |  |  |  |  |  |

\#1302 $\square$
MANUAL FEEDRATE SELECTION
\#1303

$\underbrace{\text { SPEED OVERRIDE }}_{$|  SPC  |  SPB  |  SPA  |  JV16  |  JV 8  |  JV 4  |  JV 2  |  JV 1  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |
|  MANUAL FEEDRATE OVERRIDE  |  |  |  |  |  |  |  |$}$





\#1308

| 9 BDT | 8 BDT | 7 BDT | 6 BDT | 5 BDT | 4 BDT | 3 BDT | 2 BDT |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |


\# 1312

| PLYBK | TCNT | SENSON | TLCTN | TLSKP | TLRST | ESC 1 |  |  | ESC0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PLAYBACK | CUTTINGTIMECOUNT INVALIDAT-INGSIGNAL | TOUCH <br> SWITCH <br> VALIDAT- <br> ING <br> SIGNAL | TOOL LIFE CONTROL |  |  | EXT STROKE CHECKSELECTION |  |  |  |
|  |  |  |  |  |  | 0 | 0 |  |  |
|  |  |  |  |  |  | 0 | 1 |  |  |
|  |  |  |  |  |  | 1 | 0 |  |  |
|  |  |  |  |  |  | 1 | 1 |  |  |



| \#1315 | MANINT | SSM | PPR |  | NTCRQ | TCFIN |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | SIGNAL FOR MACHINING INTERRUPTING POINT RETURN COMPLETION | $\begin{aligned} & \text { SENSOR } \\ & \text { STOP } \end{aligned}$ MODE | MACHINING TNTER RUPTING POINT RETURN MODE |  | REQUEST <br> FOR NEW TOOL <br> REPLACE- <br> MENT | COMPLE- <br> TION OF TOOL <br> REPLACE- <br> MENT |  |  |
| \#1316 | FFIN | FIN |  | EOP | ERS | EXTC | STLK | MRD |
|  | CANNED <br> CYCLE <br> FIN | MST COMPLETION |  | END <br> PROGRAM | EXTERNAL RESET | EXTERNAL <br> TIME <br> COUNT | CYCLE <br> START <br> INTERLOCK | MACHINE READY COMPLETED |

## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)

-O- <Output to NC>

| \#1317 | D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | S-INV | S-FIN |  | SAGR | SOR | GRB | GRA | GST |
|  | SPINDLE REVERSE | S CODE COMPLETED | SPINDLE STOP | SPINDLE COINCIDENCE | SPINDLE ORIENTATION | GEAR | CTION | GEAR <br> SHIFT |


| \# 1318 | ERR 2 | ERR 1 | ERR 0 | SENS(G) | SENS(T) | EXOUT | EXVER | EXIN |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | DEC TO STOP | $\begin{aligned} & \text { TMMEDI } \\ & \text { ATE ST } \end{aligned}$ | $\begin{aligned} & \text { BLOCK } \\ & \text { STOP } \end{aligned}$ | ELMINATE | TOOL <br> BREAKAGE | EXTERNAL OUTPUT | EXTERNAL VERIFY | EXTERNAL INPUT |


\#1319 | $*-\mathrm{L} \boldsymbol{\alpha}$ | $*+\mathrm{L} \boldsymbol{\alpha}$ | $*-\mathrm{LZ}$ | $*+\mathrm{LZ}$ | $*-\mathrm{LY}$ | $*+\mathrm{LY}$ | $*-\mathrm{LX}$ | $*+\mathrm{LX}$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

OVERTRAVEL
\#1320


EXTERNAL DECELERATION
\#1322

| SONPB |  |  |  | $*$ SVOF $\boldsymbol{\alpha}$ | $*$ SVOFZ |
| :--- | :--- | :--- | :--- | :--- | :--- |
| SERVO <br> POWER <br> ON | $*$ SVOFY | $*$ SVOFX |  |  |  |

\#1323

| UI 7 | UI 6 | UI 5 | UI 4 | UI 3 | UI 2 | UI 1 | UI 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

MACRO PROGRAM
\#1 32

| Ul15 | UI14 | Ul13 | UI12 | UI11 | UI10 | UI 9 | UI 8 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| MACRO PROGRAM |  |  |  |  |  |  |  |


\#1325 | ED 7 | ED6 | ED 5 | ED 4 | ED 3 | ED 2 | ED 1 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | ED 0 |  |  |  |  |  |



## APPENDIX 2 I/O LIST FOR YASNAC J50M (FOR MACHINING CENTERS) (Cont'd)

| - | <Output D 7 | D 6 | D 5 | D 4 | D 3 | D 2 | D 1 | D 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| \#1341 | SID8 | SID7 | SID6 | SID5 | SID4 | SID3 | SID2 | SID1 |



## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M

< Internal Relays >


## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)




# APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd) 




## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)




## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)


< Register >

\#1885 0 保
\#1 9 0 0 0 $\quad \square$


## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)

- < Sequencer Parameter >




## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)




## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)



| \# 71 | 1 | 5 | 0 |  |
| :--- | :--- | :--- | :--- | :--- |
| \# | 1 | 5 | 1 | $\square$ |
| $\#$ | 7 | 1 | 5 | 2 |
| \# |  |  |  |  |



## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)





## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)






## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)




| $\begin{aligned} & \# 785 \\ & \# 78 \\ & \# \end{aligned}$ |  |
| :---: | :---: |
| \# 7852 |  |
| \# 7853 |  |
| \# 7854 |  |
| \# 7855 |  |
| \#7856 |  |
| \# 7857 |  |
| \# 7858 |  |
| \# 7859 |  |
| \#7860 |  |
| \# 7861 |  |
| \# 7862 |  |
| \#7863 |  |
| \#7864 |  |
| \# 7865 |  |
| \# 7866 |  |
| \#7867 |  |
| \#7868 |  |
| \# 7869 |  |
| \# 7870 |  |
| \# 7871 |  |
| \#7872 |  |
| \# 7873 |  |
| \# 7874 |  |
| \# 7875 |  |
| \# 7876 |  |
| \# 7877 |  |
| \# 7878 |  |
| \# 7879 |  |
| \# 7880 |  |
| \# 7881 |  |
| \# 7882 |  |
| \#7883 |  |
| \# 7884 |  |
| \#7885 |  |
| \#7886 |  |
| \# 7887 |  |
| \# 7888 |  |
| \# 7889 |  |
| \# 7890 |  |
| \# 7891 |  |
| \# 7892 |  |
| \# 7893 |  |
| \# 7894 |  |
| \# 7895 |  |
| \# 7896 |  |
| \# 7897 |  |
| \# 7898 |  |
| \# 7899 |  |

## APPENDIX 3 LIST OF INTERNAL RELAYS, REGISTERS FOR YASNAC J50L/J50M (Cont'd)



## APPENDIX 4 CONVERSION TABLE OF DECIMAL AND HEXADECIMAL NOTATION

| Hex | Dec | Hex | Dex | Hex | Dec | Hex | Dec | Hex | Dec | Hex | Dec | Hex | Dec | Hex | Dec |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 00 | 0 | 20 | 32 | 40 | 64 | 60 | 96 | 80 | 128 | A 0 | 160 | C 0 | 192 | E 0 | 224 |
| 01 | 1 | 21 | 33 | 41 | 65 | 61 | 97 | 81 | 129 | A 1 | 161 | C 1 | 193 | E 1 | 225 |
| 02 | 2 | 22 | 34 | 42 | 66 | 62 | 98 | 82 | 130 | A 2 | 162 | C 2 | 194 | E 2 | 226 |
| 03 | 3 | 23 | 35 | 43 | 67 | 63 | 99 | 83 | 131 | A 3 | 163 | C 3 | 195 | E 3 | 227 |
| 04 | 4 | 24 | 36 | 44 | 68 | 64 | 100 | 84 | 132 | A 4 | 164 | C 4 | 196 | E 4 | 228 |
| 05 | 5 | 25 | 37 | 45 | 69 | 65 | 101 | 85 | 133 | A 5 | 165 | C 5 | 197 | E 5 | 229 |
| 06 | 6 | 26 | 38 | 46 | 70 | 66 | 102 | 86 | 134 | A 6 | 166 | C 6 | 198 | E 6 | 230 |
| 07 | 7 | 27 | 39 | 47 | 71 | 67 | 103 | 87 | 135 | A 7 | 167 | C 7 | 199 | E 7 | 231 |
| 08 | 8 | 28 | 40 | 48 | 72 | 68 | 104 | 88 | 136 | A 8 | 168 | C 8 | 200 | E 8 | 232 |
| 09 | 9 | 29 | 41 | 49 | 73 | 69 | 105 | 89 | 137 | A 9 | 169 | C 9 | 201 | E 9 | 233 |
| 0 A | 10 | 2 A | 42 | 4 A | 74 | 6 A | 106 | 8 A | 138 | A A | 170 | C A | 202 | E A | 234 |
| 0 B | 11 | 2 B | 43 | 4 B | 75 | 6 B | 107 | 8 B | 139 | A B | 171 | C B | 203 | E B | 235 |
| 0 C | 12 | 2 C | 44 | 4 C | 76 | 6 C | 108 | 8 C | 140 | A C | 172 | C C | 204 | EC | 236 |
| 0 D | 13 | 2 D | 45 | 4 D | 77 | 6 D | 109 | 8 D | 141 | A D | 173 | C D | 205 | E D | 237 |
| OE | 14 | 2 E | 46 | 4 E | 78 | 6 E | 110 | 8 E | 142 | A E | 174 | C E | 206 | E E | 238 |
| 0 F | 15 | 2 F | 4.7 | 4 F | 79 | 6 F | 111 | 8 F | 143 | AF | 175 | C F | 207 | E F | 239 |
| 10 | 16 | 30 | 48 | 50 | 80 | 70 | 112 | 90 | 144 | B 0 | 176 | D 0 | 208 | F 0 | 240 |
| 11 | 17 | 31 | 49 | 51 | 81 | 71 | 113 | 91 | 145 | B 1 | 177 | D 1 | 209 | F 1 | 241 |
| 12 | 18 | 32 | 50 | 52 | 82 | 72 | 114 | 92 | 146 | B 2 | 178 | D 2 | 210 | F 2 | 242 |
| 13 | 19 | 33 | 51 | 53 | 83 | 73 | 115 | 93 | 147 | B 3 | 179 | D 3 | 211 | F 3 | 243 |
| 14 | 20 | 34 | 52 | 54 | 84 | 74 | 116 | 94 | 148 | B 4 | 180 | D 4 | 212 | F 4 | 244 |
| 15 | 21 | 35 | 53 | 55 | 85 | 75 | 117 | 95 | 149 | B 5 | 181 | D 5 | 213 | F 5 | 245 |
| 16 | 22 | 36 | 54 | 56 | 86 | 76 | 118 | 96 | 150 | B 6 | 182 | D 6 | 214 | F 6 | 246 |
| 17 | 23 | 37 | 55 | 57 | 87 | 77 | 119 | 97 | 151 | B 7 | 183 | D 7 | 215 | F 7 | 247 |
| 18 | 24 | 38 | 56 | 58 | 88 | 78 | 120 | 98 | 152 | B 8 | 184 | D 8 | 216 | F 8 | 248 |
| 19 | 25 | 39 | 57 | 59 | 89 | 79 | 121 | 99 | 153 | B 9 | 185 | D 9 | 217 | F 9 | 249 |
| 1 A | 26 | 3 A | 58 | 5 A | 90 | 7 A | 122 | 9 A | 154 | B A | 186 | D A | 218 | FA | 250 |
| 1 B | 27 | 3 B | 59 | 5 B | 91 | 7 B | 123 | 9 B | 155 | B B | 187 | D B | 219 | F B | 251 |
| 1 C | 28 | 3 C | 60 | 5 C | 92 | 7 C | 124 | 9 C | 156 | B C | 188 | D C | 220 | FC | 252 |
| 1 D | 29 | 3 D | 61 | 5 D | 93 | 7 D | 125 | 9 D | 157 | B D | 189 | D D | 221 | F D | 253 |
| 1 E | 30 | 3 E | 62 | 5 E | 94 | 7 E | 126 | 9 E | 158 | B E | 190 | D E | 222 | F E | 254 |
| 1 F | 3 | 3 F | 63 | 5 F | 95 | 7 F | 127 | 9 F | 159 | B F | 191 | D F | 223 | F F | 255 |

## YASNAC J50 PC SYSTEM INSTRUCTIONS

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[^0]:    Upon receipt of the product and prior to initial operation, read these instructions thoroughly, and retain for future reference.

[^1]:    \#1500 = \#1502 ... 21 is set.
    \#1500 = \#1502 ... Z1 is cleared.

[^2]:    Note: Generally, the parameter mode of MODE 4 is later entered to clear the edit area, followed by the storing of the list tape in the list tape mode of MODE 2. For more details, refer to par. 9.14, "OPERATING PROCEDURE."

